

Model Name: GA-P55M-UD2 1.0

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU LGA1156-A
05	CPU LGA1156-B
06	CPU LGA1156-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	DDR III POWER CAP
10	PCI EXPRESS*16 SLOT
11	PCH FDI,DMI,USB,PCIE,NVRAM
12	PCH DP,CLK BUFFER
13	PCH HOST,SATA,PCI
14	PCH GPIO,CTRL,AUDIO
15	PCH PWR,GND
16	PCI EXPRESS*4 SLOT
17	PCI EXPRESS*1 SLOTS X3
18	PCI SLOT X2
19	ITE 8720 LPC IO
20	COM, -PROHOT , DYNAMIC OC , LPT
21	Dual BIOS
22	ALC888/889A
23	REAR AUDIO JACK
24	CLOCK GEN ICS9LPRS914
25	VCORE PWM ISL6334CR-1
26	VCORE PWM ISL6334CR-2
27	DISCRETE POWER I

SHEET TITLE

28	DDR 15V & VCC1 05 PCH PWM ISL6545CBZ
29	CPU VAXG PWM ISL6314CRZ
30	CPU VTT PWM ISL6322G
31	F PANEL , F USB , FDD
32	ATX POWER
33	Marvell 88SE9123
34	REALTEK RTL8111DL
35	REALTEK RTL8111DL 1
36	TI TSB43AB23 1394
37	HWM,KB/MS , FAN CTRL
38	TPM SLB9635TT
39	ESATA JMB362
40	TABLE LIST
41	
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Gigabyte Technology		
Title		
Cover Sheet		
Size	Document Number	Rev
Custom	GA-P55M-UD2	1.0
Date:	Thursday, July 09, 2009	Sheet 1 of 33

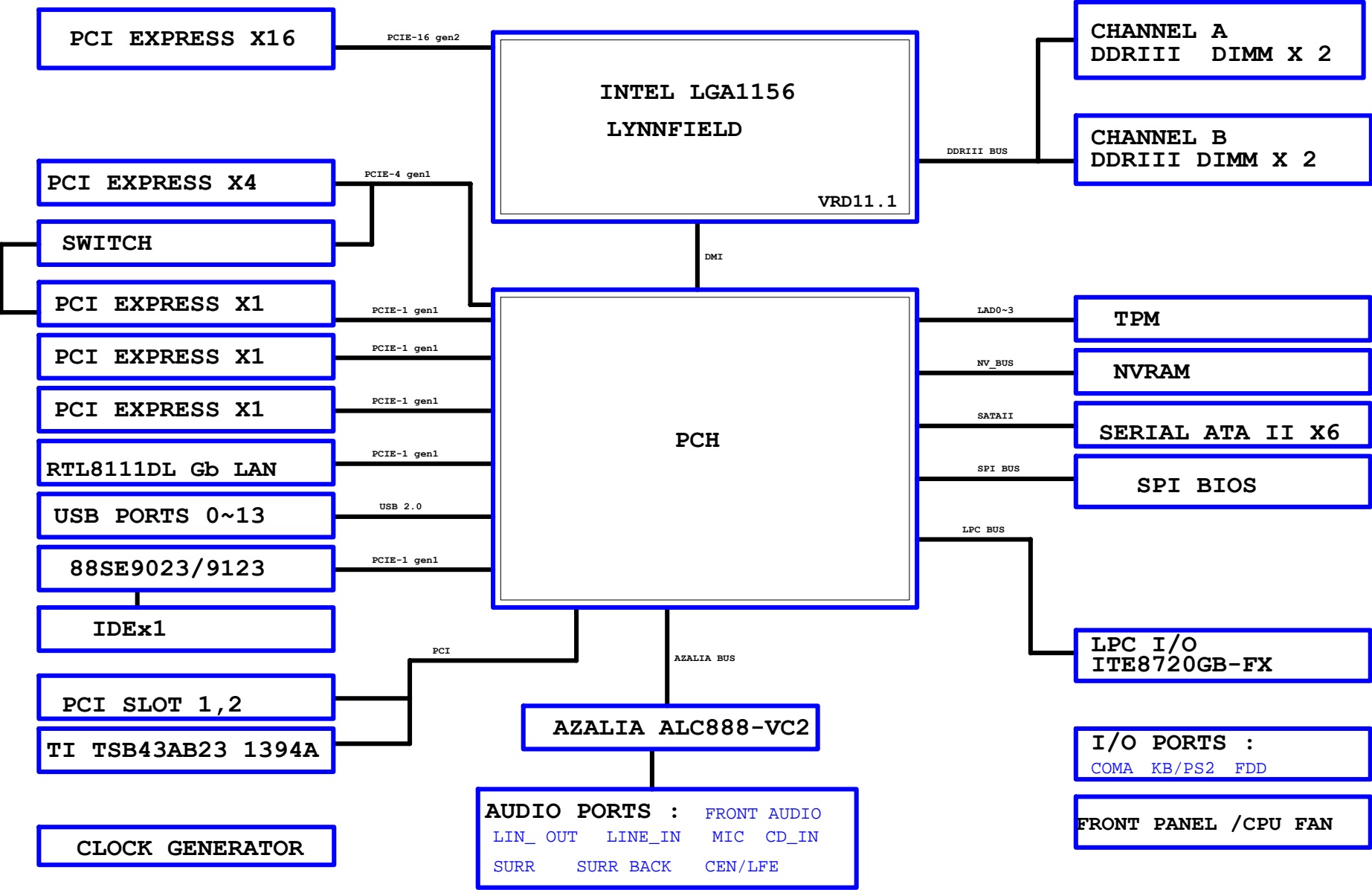
Component value change history

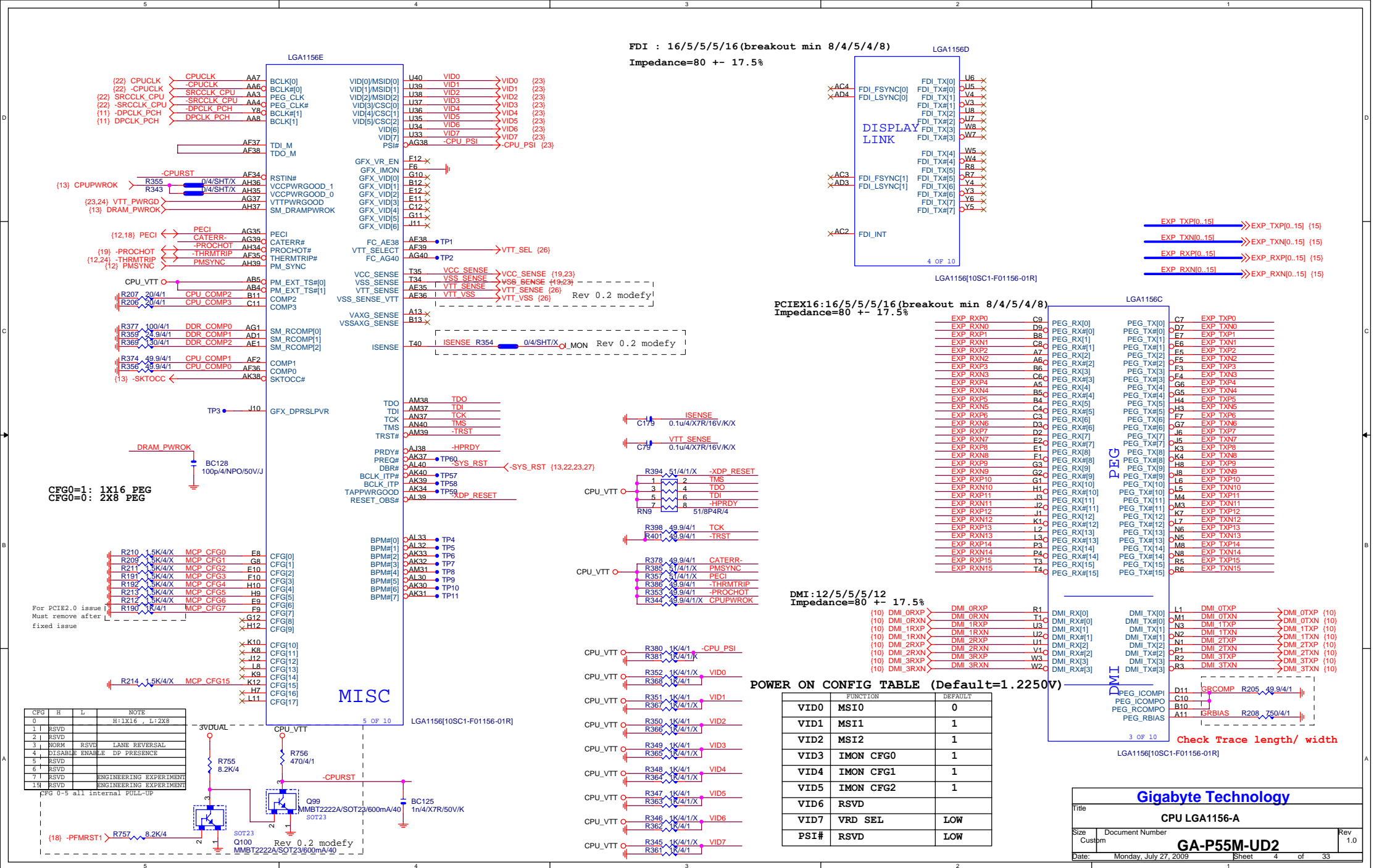
Data	Change Item	Reason
0.1	1. 9ME55QD3R-00-01	
EP55A-UD3P 0.1	1. Add R687,BC241 For ISL8014 VDD PIN	
	2. IDE Conn. change 直立式	
	3. ADD CPU RM	
	4. CPU_VAXG R153 --> R151 68K/4/1 , R127 20K/4/1 --> 42K/4/1 , BC60 0.1u --> 1n/4	
	5. DIMM & LPT COLOR --> BLUE	
	6. REMOVE 7474 , CHANGE TO ITE8720/JX	
	7. CLK阻值調整	
	8. PCIEX16 CHANGE TO RIGHT EJECTOR	
	9. 0.5uH --> 1.0uH 含阻值修改	
EP55A-UD3P 0.2	1. CHECK +12V SHORT PAD FOR 10mil	
	2. F1_1394加蓋(包材階)	
	3. ADD DR86=124K/4/1 , DR88=249K/4/1	
	4. U12~U15 upi6262M --> upi6267M	
	5. DRAM_PWROK R490=1K/4/1 , R491=3K/4/1	
	6. REMOVE DR138=0/4 , ADD DR139,DR141=0/4	
	7. RU2指定料號:10HP4-112540-11R	
	8. 排阻指定廠牌用WALSIN	
	9. LU1,LU4 RTL8111D --> RTL8111C	
	10. PCB "育富"移除	
	11. ONFI指定用11SM1-600078-02R	
	12. R376 2.26K/4/1 --> 2.55K/4/1	
	13. 確認上哪種upi6262 10%/upi6267?	
	14. FB7~FB10 REMOVE	
	15. PCH BUFFER 25MHz REMOVE "X2,C93,C94,R420"	
	16. BIOS 16M --> 32M (FOR ONFI ONBOARD)	
		10B 1. ADD R190 1K/4/1 FOR PCI-E OVER-CLOCK
P55M-UD4	1. RTL8111C --> RTC8111D (是否上新板VB?)	
P55M-UD2 0.2	1. ADD PACKAGE F1 1394 2. P55 REV.B1 --> REV.B2 3. VCC1_05_PCH --> OP+MOS	
P55M-UD2 1.0	1. CHECK POWER SEQUENCY FOR VCC1_05_PCH 2. PROCHOT阻值修改 3. REMOVE PLL 22U 4. Q73,Q74 UPA2724 --> UPA2726	

Circuit or PCB layout change
for next version

DATE	Change Item	Reason
EP55-UD3R 0.1	1. 9ME55QD3R-00-01	
EP55A-UD3P 0.1	1. FWR_LED GPIO46 --> GPIO20	
	2. CHECK DDR3 LAYER RULE UPDATE (DDR3 LIBRARY UPDATE)	
	3. ADD F_PANEL RESET & BOTTOM ESD PROTECT	
	4. CPU ISENSE & IOUT ADD CONTROL CIRCUIT	
	5. RESET 7474 COST DOWN CHECK	
	6. SRCCLK_CKG TRACE CHANGE TO TOP	
	7. USBP12 , USBP13 +- SWAP NET	
	8. +12V PROTECT Q91 ADD Damping Resistor "R748"	
	9. 零件位置BAT移出至BAT外框	
	10. X3與CLR_CMOS距離不足5mm	
	11. Remove 0 ohm	
	12. CPU_VTT ADD背面電容SBC9,SBC10	
	13. CPU_VTT ADD VTT_SENSE , VTT_VSS	
	14. MODIFY PCIEX4 & PCIEX1 SELECT "-4X_EN"	
	15. ADD ESATA JMB362	
	16. ITE8275 PATCH -SYS_RST 漏電	
	17. DRAM_PWROK 留Damping 電阻	
	18. DRIVER的PIN5 GND走線與GATE同粗	
	19. LAN CTRL18為PHASE須在第二層做隔離	
EP55A-UD3P 0.2	1. REMOVE AUDIO ESD	
	2. REMOVE CPU_VAXG	
	3. PCH_CLK 改 SHORT PAD (0 ohm維持10pcs以下)	
	4. ITE8275 GPIO11,GPIO13 TO TURBO0/TURBO1	
	5. F_PANEL UPDATE H2X10PANEL-1	
	6. ONFI 改 ON BOARD	
	7. 1394 "IEC1" NET SWAP & SHORT PROTECT	
	8. PIN HEATER CHECK	
	9. ITE8275 SYS_RST PATCH	
	10. BC118,BC119 --> TBC29,TBC30	
	11. BC5靠近M_BIOS PIN8 , BC6靠近M_BIOS PIN8	
	12. U2 7474 REMOVE	

BLOCK DIAGRAM





LGA1156A

MAAA0	AW18	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY15	SA_MA[1]	SA_DQS[0]	CA13	-DQSA0
MAAA2	AV15	SA_MA[2]	SA_DM[0]	CA12	DMA0
MAAA3	AU15	SA_MA[3]		AH1	MDA0
MAAA4	AW14	SA_MA[4]	SA_DQ[0]	AJ4	MDA1
MAAA5	AY13	SA_MA[5]	SA_DQ[1]	AL2	MDA2
MAAA6	AV14	SA_MA[6]	SA_DQ[2]	AL1	MDA3
MAAA7	AW13	SA_MA[7]	SA_DQ[3]	AG2	MDA4
MAAA8	AU14	SA_MA[8]	SA_DQ[4]	AH2	MDA5
MAAA9	AW12	SA_MA[9]	SA_DQ[5]	AK1	MDA6
MAAA10	AT19	SA_MA[10]	SA_DQ[6]	AK2	MDA7
MAAA11	AU13	SA_MA[11]	SA_DQ[7]		
MAAA12	AW11	SA_MA[12]		AP2	DQSA1
MAAA13	AU24	SA_MA[13]	SA_DQS[1]	AP3	-DQSA1
MAAA14	AT11	SA_MA[14]	SA_DQS[1]	AN1	DMA1
MAAA15	AR10	SA_MA[15]	SA_DM[1]		
(7) -SWEA	-SWEA	AT22	SA_WE#	AN3	MDA8
(7) -SCASA	-SCASA	AU22	SA_CAS#	AN2	MDA9
(7) -SRASA	-SRASA	AT20	SA_RAS#	AR3	MDA10
(7) SBAA0	SBAA0	AV20	SA_BS[0]	AR2	MDA12
(7) SBAA1	SBAA1	AU19	SA_BS[0]	AM3	MDA11
(7) SBAA2	SBAA2	AU12	SA_BS[1]	AM2	MDA13
			SA_BS[2]	AP1	MDA14
				AR4	MDA15
(7) -CSA0	-CSA0	AV21	SA_CS#0		
(7) -CSA1	-CSA1	AW24	SA_CS#1	AL4	DQSA2
(7) -CSA2	-CSA2	AU21	SA_CS#2	AL3	-DQSA2
(7) -CSA3	-CSA3	AU23	SA_CS#3	AL1	DMA2
(7) CKEA0	CKEA0	AU10	SA_CKE[0]	AT4	MDA16
(7) CKEA1	CKEA1	AW10	SA_CKE[1]	AJ2	MDA17
(7) CKEA2	CKEA2	AV10	SA_CKE[2]	AW3	MDA18
(7) CKEA3	CKEA3	AY10	SA_CKE[3]	AW4	MDA19
				AT3	MDA20
				AT1	MDA21
MODT_A0	AV23	SA_ODT[0]		AV2	MDA22
MODT_A1	AV24	SA_ODT[1]		AV4	MDA23
MODT_A2	AW23	SA_ODT[2]			
MODT_A3	AY24	SA_ODT[3]			
				AY6	DQSA3
(7) DCLKA0	DCLKA0	AR22	SA_CK[0]	AW6	-DQSA3
(7) -DCLKA0	-DCLKA0	AR21	SA_CK#0	AW6	DMA3
(7) DCLKA1	DCLKA1	AP18	SA_CK[1]		
(7) -DCLKA1	-DCLKA1	AN18	SA_CK#1	AW5	MDA24
(7) DCLKA2	DCLKA2	AN21	SA_CK[2]	AY5	MDA25
(7) -DCLKA2	-DCLKA2	AP21	SA_CK#2	AJ8	MDA26
(7) DCLKA3	DCLKA3	AP19	SA_CK[3]	AY8	MDA27
(7) -DCLKA3	-DCLKA3	AN19	SA_CK#3	AJ5	MDA28
(7,8) -DDR3_RST	-DDR3_RST	AV8	SM_DRAMRST#	AV6	MDA29
				AW7	MDA30
				AW7	MDA31
TP1	AK22	SA_CS#4		AR28	DQSA4
TP1	AK22	SA_CS#5	SA_DQS[4]	AT29	-DQSA4
TP1	AL23	SA_CS#6	SA_DM[4]	AN29	DMA4
TP1	AK23	SA_CS#7			
				AN27	MDA32
				AT28	MDA33
				AP28	MDA34
				AP30	MDA35
				AN26	MDA36
				AR27	MDA37
				AR29	MDA38
				AN30	MDA39
				AV32	DQSA5
				AW32	-DQSA5
				AW31	DMA5
				AL30	MDA40
				AL31	MDA41
				AL33	MDA42
				AL34	MDA43
				AV30	MDA44
				AW30	MDA45
				AL33	MDA46
				AW33	MDA47
				AW36	DQSA6
				AW35	-DQSA6
				AW35	DMA6
				AW35	MDA48
				AY35	MDA49
				AV37	MDA50
				AL37	MDA51
				AY34	MDA52
				AW34	MDA53
				AW36	MDA54
				AW37	MDA55
				AR38	DQSA7
				AR38	-DQSA7
				AT38	DMA7
				AT39	MDA56
				AT40	MDA57
				AN38	MDA58
				AN38	MDA59
				AL38	MDA60
				AP39	MDA61
				AP39	MDA62
				AP40	MDA63

DDR_A

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LGA1156[10SC1-F01156-01R]

LGA1156B

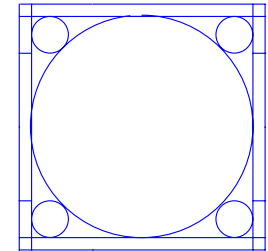
	MAAB0	AU20	SB_MA[0]	AF4	DQSB0
	MAAB1	AU18	SB_MA[1]	AE5	-DQSB0
	MAAB2	AV18	SB_MA[2]	AE4	DMB0
	MAAB3	AU17	SB_MA[3]		
	MAAB4	AY18	SB_MA[4]	AD7	MDB0
	MAAB5	AV17	SB_MA[5]	AD6	MDB1
	MAAB6	AW17	SB_MA[6]	AH8	MDB2
	MAAB7	AU16	SB_MA[7]	AJ8	MDB3
	MAAB8	AT17	SB_MA[8]	AC7	MDB4
	MAAB9	AY16	SB_MA[9]	AC6	MDB5
	MAAB10	AY25	SB_MA[10]	AF5	MDB6
	MAAB11	AW16	SB_MA[11]	AE6	MDB7
	MAAB12	AW15	SB_MA[12]		
	MAAB13	AW28	SB_MA[13]	AH6	DQSB1
	MAAB14	AY12	SB_MA[14]	AJ5	-DQSB1
	MAAB15	AU11	SB_MA[15]	AH4	DMB1
(8) -SWEB	-SWEB	AU26	SB_WE#	AG5	MDB8
(8) -SCASB	-SCASB	AW22	SB_CAS#	AH7	MDB9
(8) -SRASB	-SRASB	AW26	SB_RAS#	AK6	MDB10
(8) SBAB0	SBAB0	AU25	SB_BS[0]	AL4	MDB11
(8) SBAB1	SBAB1	AW25	SB_BS[1]	AL4	MDB12
(8) SBAB2	SBAB2	AV12	SB_BS[2]	AL7	MDB14
(8) -CSB0	-CSB0	AY27	SB_CS#0	AK7	MDB15
(8) -CSB1	-CSB1	AW29	SB_CS#1		
(8) -CSB2	-CSB2	AV26	SB_CS#2	AN6	DQSB2
(8) -CSB3	-CSB3	AV29	SB_CS#3	AM6	-DQSB2
(8) CKEB0	CKEB0	AW8	SB_CKE[0]	AM7	DMB2
(8) CKEB1	CKEB1	AY9	SB_CKE[1]	AL6	MDB16
(8) CKEB2	CKEB2	AU9	SB_CKE[2]	AN5	MDB17
(8) CKEB3	CKEB3	AV9	SB_CKE[3]	AP6	MDB18
				AR5	MDB19
				AL5	MDB20
				AM4	MDB21
				AN7	MDB22
				AP5	MDB23
				AR8	DQSB3
				AP8	-DQSB3
				AT7	DMB3
(8) DCLKB0	DCLKB0	AR17	SB_CK[0]	AT6	MDB24
(8) DCLKB1	DCLKB1	AT15	SB_CK[0]	AR7	MDB25
(8) DCLKB1	DCLKB1	AR15	SB_CK[1]	AR9	MDB26
(8) DCLKB2	DCLKB2	AN17	SB_CK[2]	AM8	MDB27
(8) DCLKB2	DCLKB2	AN16	SB_CK[2]	AN8	MDB28
(8) DCLKB3	DCLKB3	AR19	SB_CK[3]	AR6	MDB29
(8) DCLKB3	DCLKB3	AR18	SB_CK[3]	AL8	MDB30
				AT9	MDB31
	TP12	AM23	SB_CS#4	AT25	DQSB4
	TP13	AM24	SB_CS#5	AR24	-DQSB4
	TP14	AL24	SB_CS#6	AN24	DMB4
	TP17	AK24	SB_CS#7		
				SB_DQ[32]	
				SB_DQ[33]	
				SB_DQ[34]	
				SB_DQ[35]	
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				SB_DQ[62]	
				SB_DQ[63]	

DDR B

DDR_B

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LGA1156[10SC1-F01156-01R]

CR
CPU RETAINION/X

Need check the new CPU ME

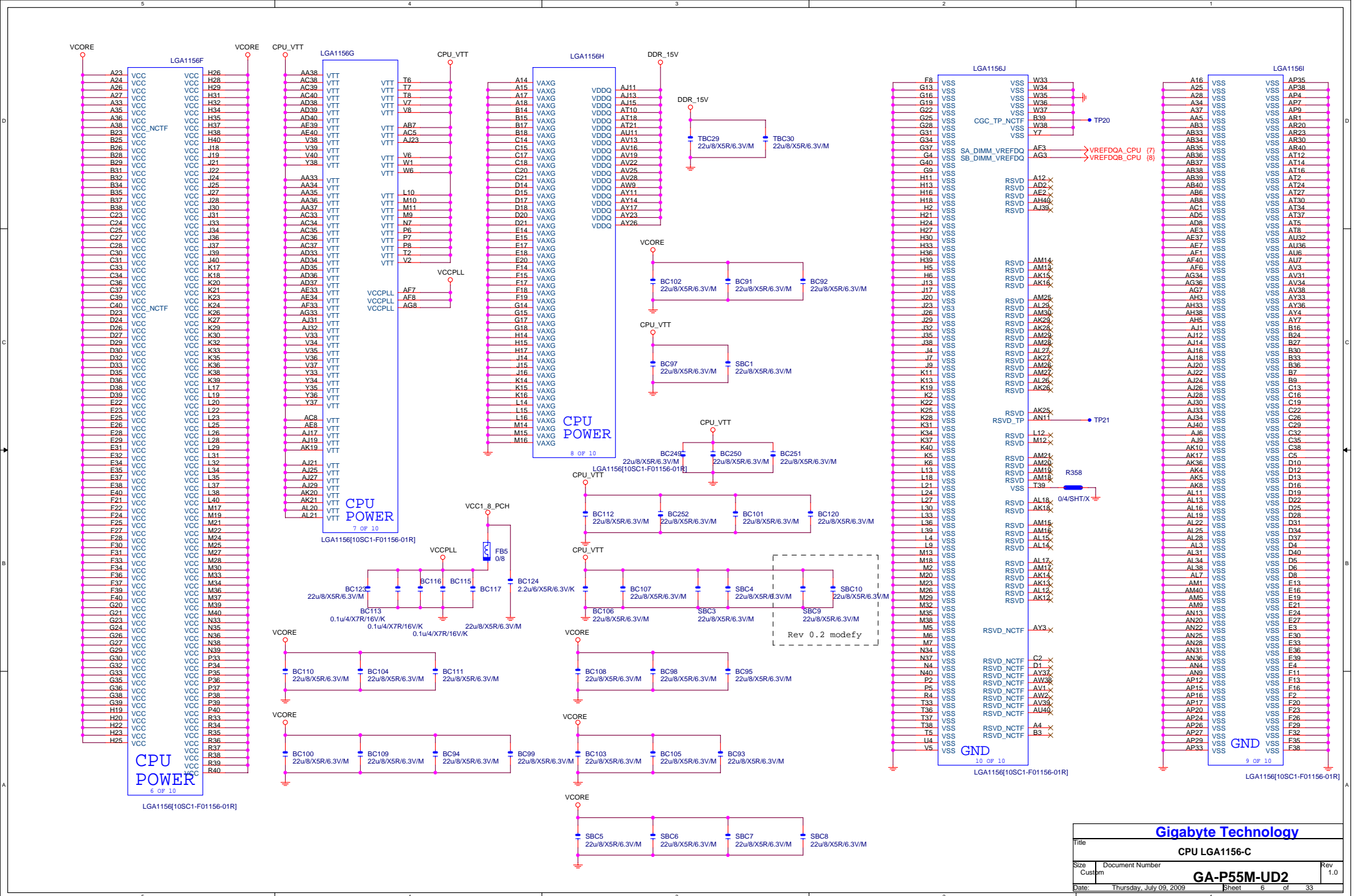
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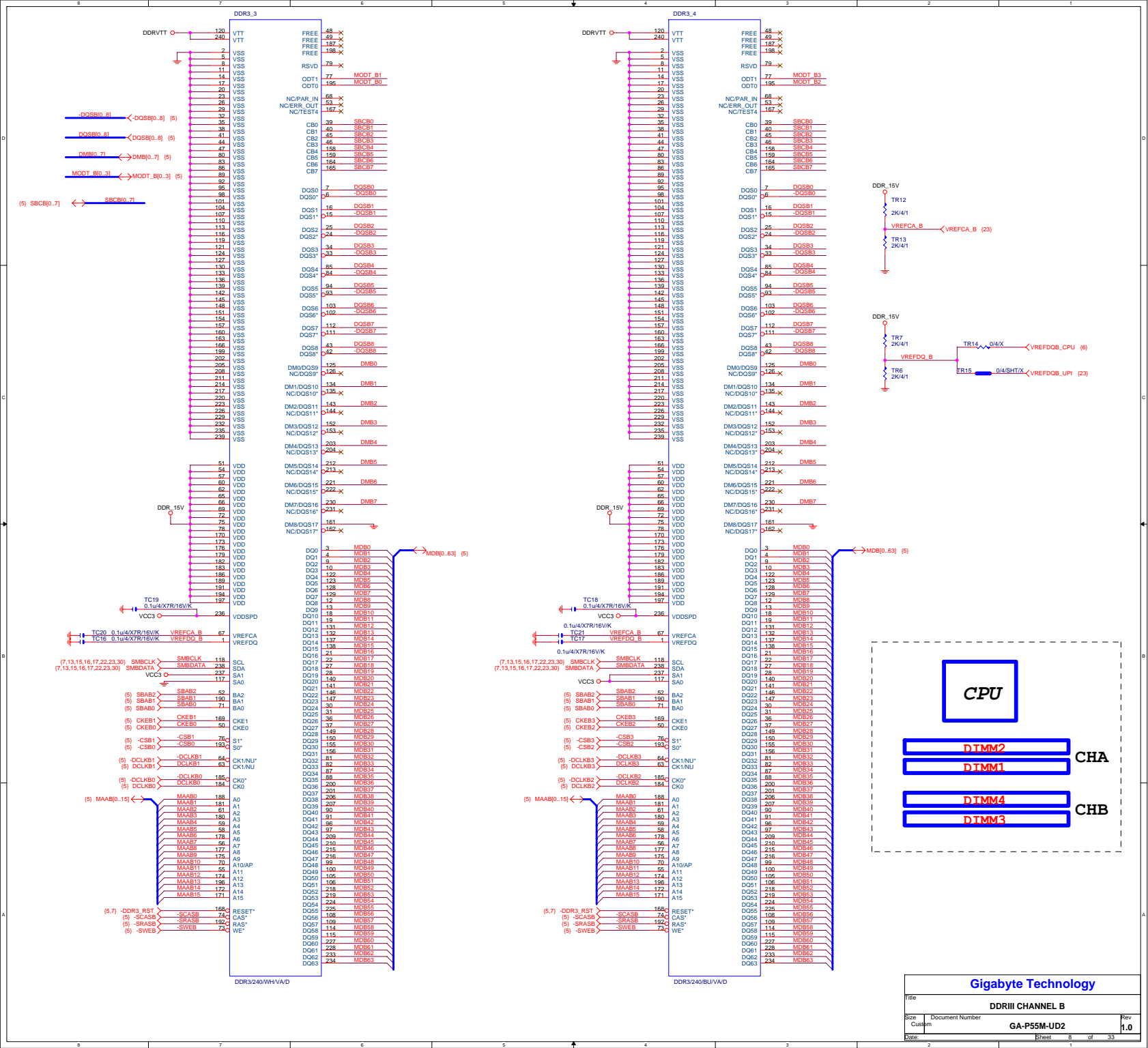


PLATE+ILM[12KRC-0F0001-01R]

Gigabyte Technology

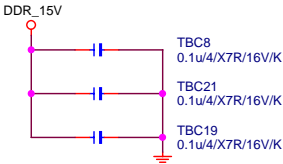
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Size			Document Number		
Custom			GA-P55M-UD2		
Date:			Thursday, July 09, 2009		
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Rev			1.0		



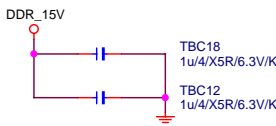
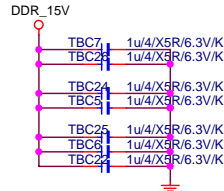
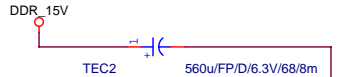
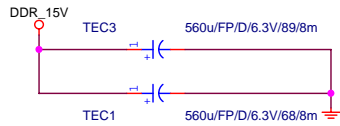
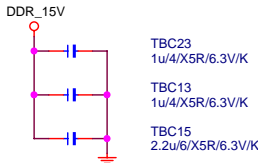
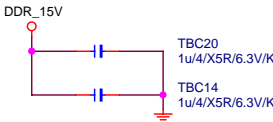
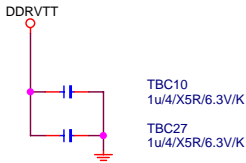
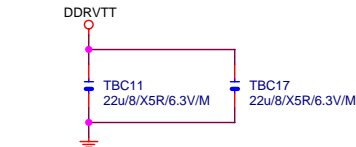
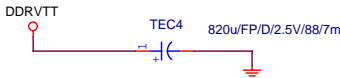
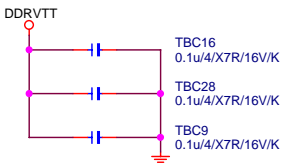


DDR TERMINATION
CHANNEL A/B

DDR15V Decouple

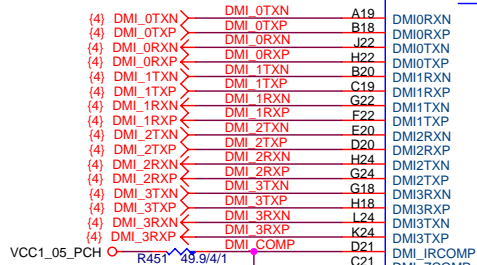


DDRVTT Decouple

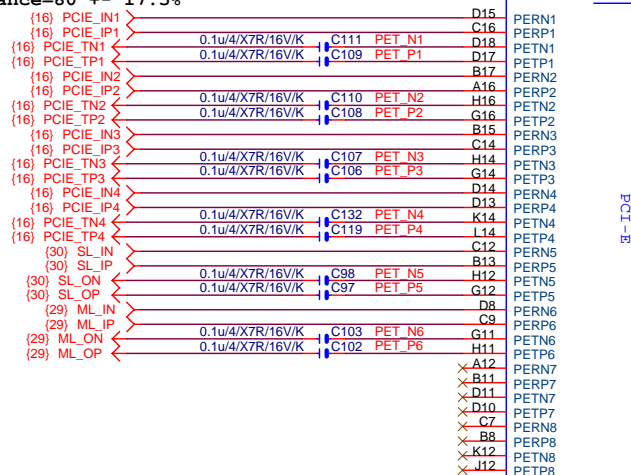


Gigabyte Technology			
Title			
DDRIII POWER CAP			
Size	Document Number		Rev
B	GA-P55M-UD2		1.0
Date:	Thursday, July 09, 2009	Sheet	9 of 33

DMI:12/5/5/5/12
Impedance=80 +- 17.5%

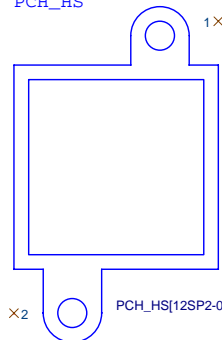


PCIE X1 :15/5/5/5/15
Impedance=80 +- 17.5%



電容要靠近 slot 端

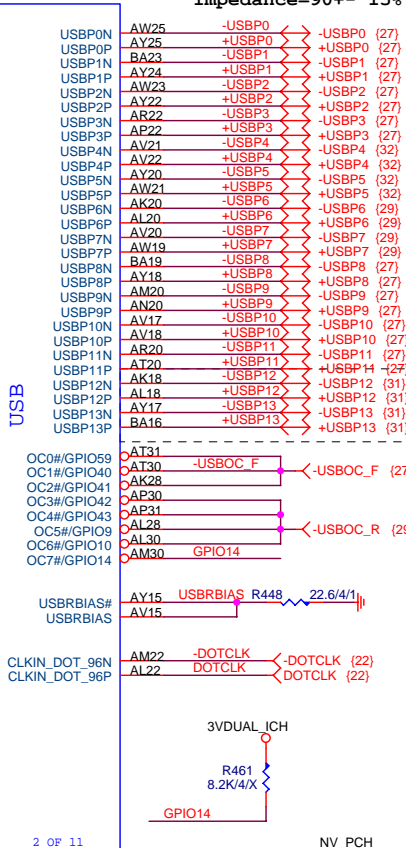
PCH_HS



USB OC# Configure	
OC0#	USB0,1
OC1#	USB2,3
OC2#	USB4,5
OC3#	USB6,7
OC4#	USB8,9
OC5_6#	USB10~13
OC7#	

PCHB

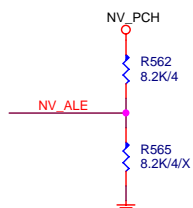
USB:15/4.5/7.5/4.5/15
Impedance=90+- 15%



USB

PCI-E

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BD82P55-B2/S



NV_ALE	
Hi	Enable Danbury
Lo	Disable Danbury

Intel anti theft techonlogy

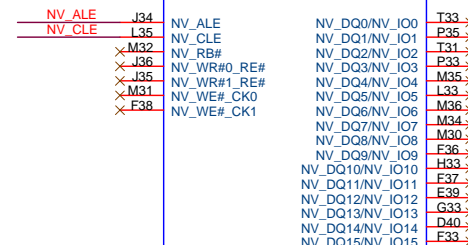
DMI Terminator voltage
HI : AC COUP : TX/RX TO VCC
LO : DC COUP : HALF SWING

Rev 0.2 modefy

OC[3:0]# for
Device 29
(ports 0-7)
OC[7:4]# for
Device 26
(ports 8-13)

Impedance=50+- 15%
ONFI: NV_DQ 4/5

NV_DQS 4/10
NV_CTRL 4/10
NV_CK 4/15

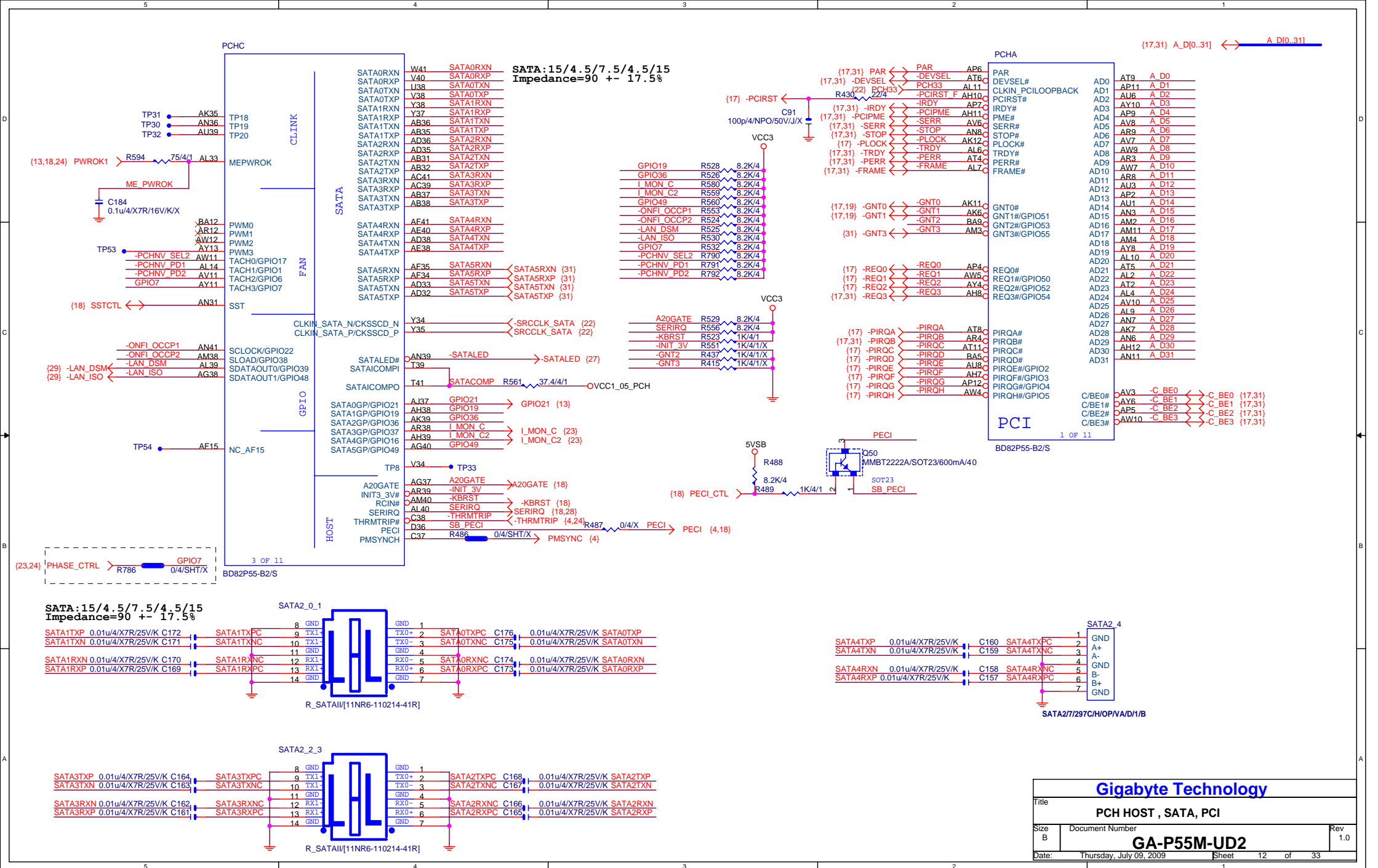


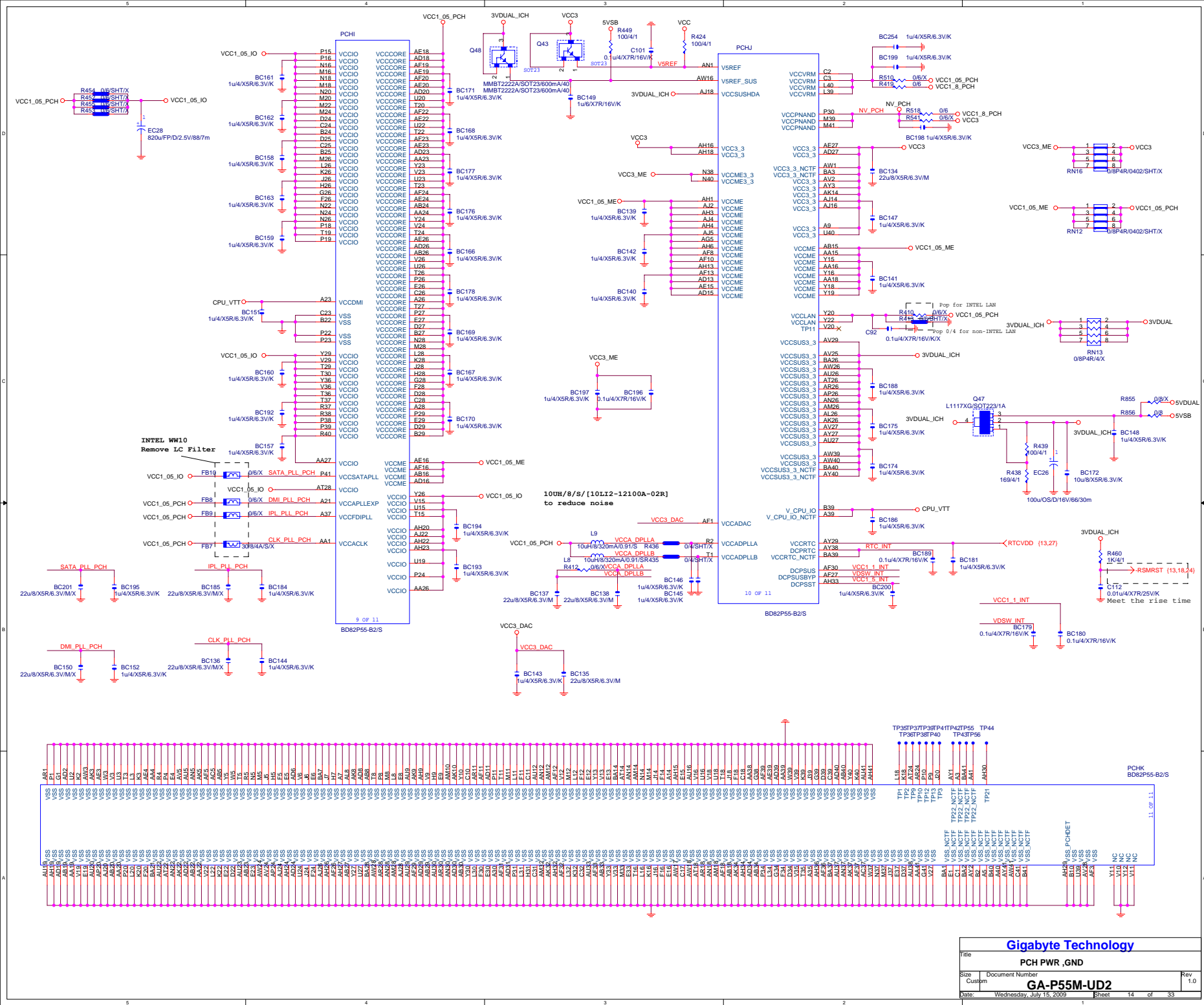
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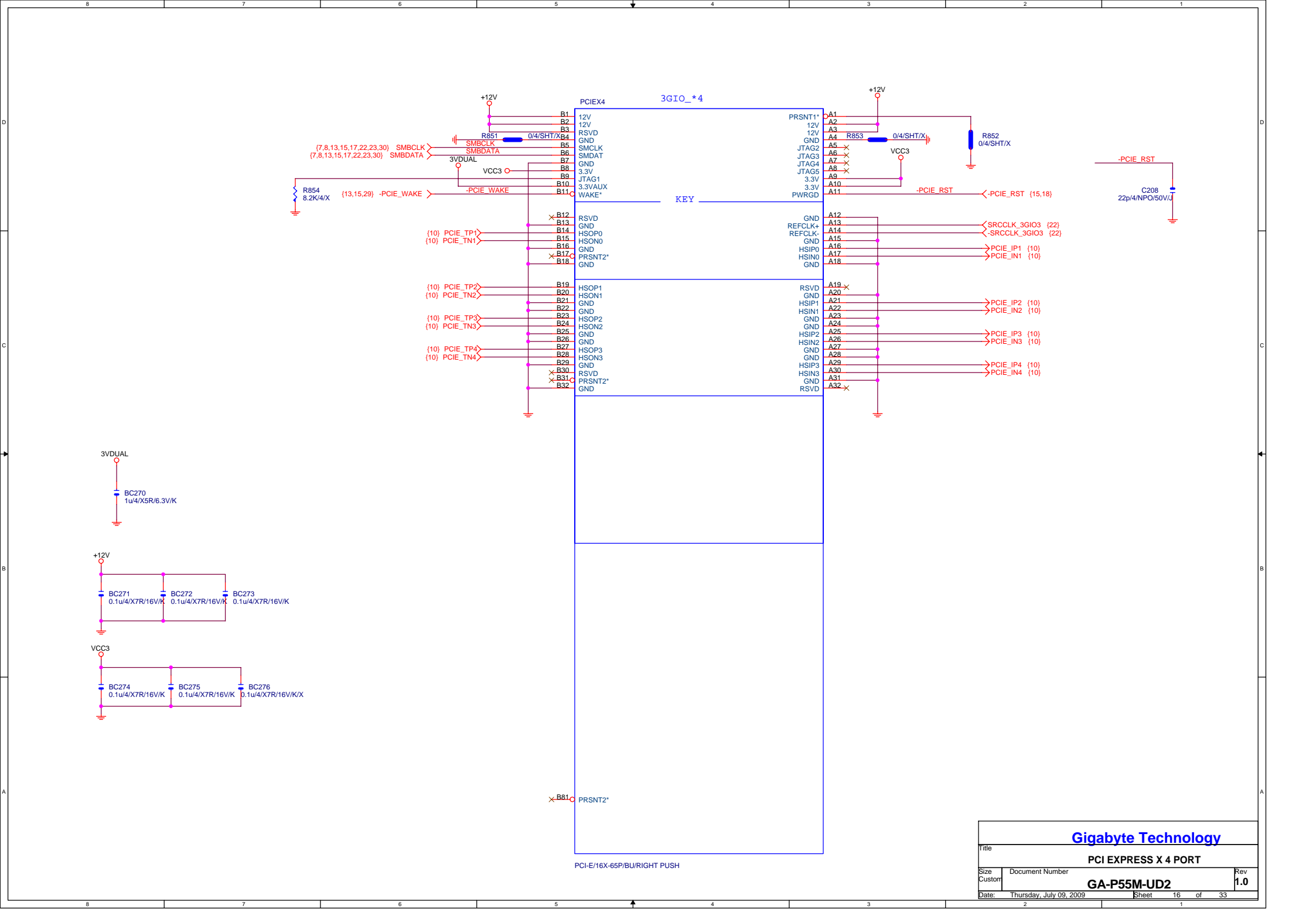
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BD82P55-B2/S

Gigabyte Technology

Title			
PCH FDI,DMI,USB ,PCIE,NVRAM			
Size	Document Number		Rev
B	GA-P55M-UD2		1.0
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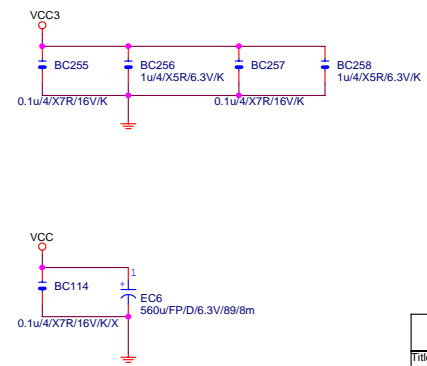
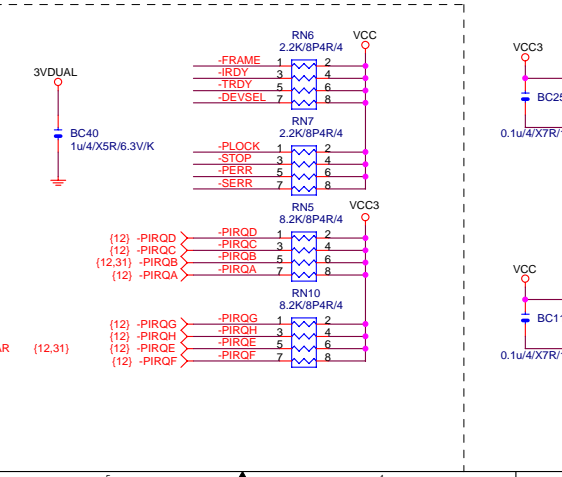
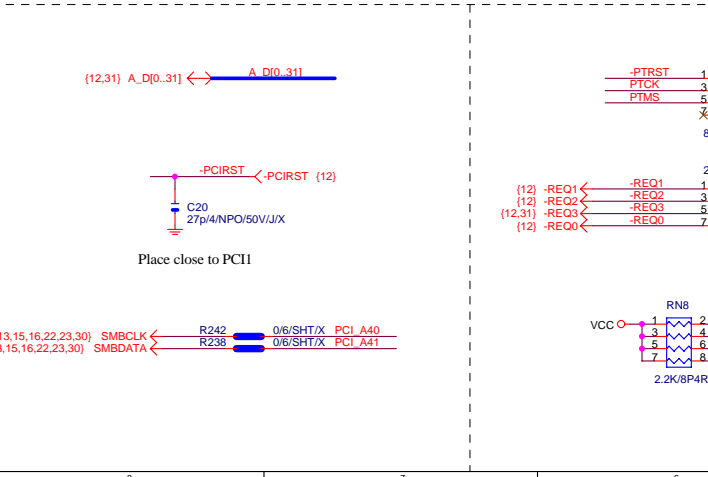
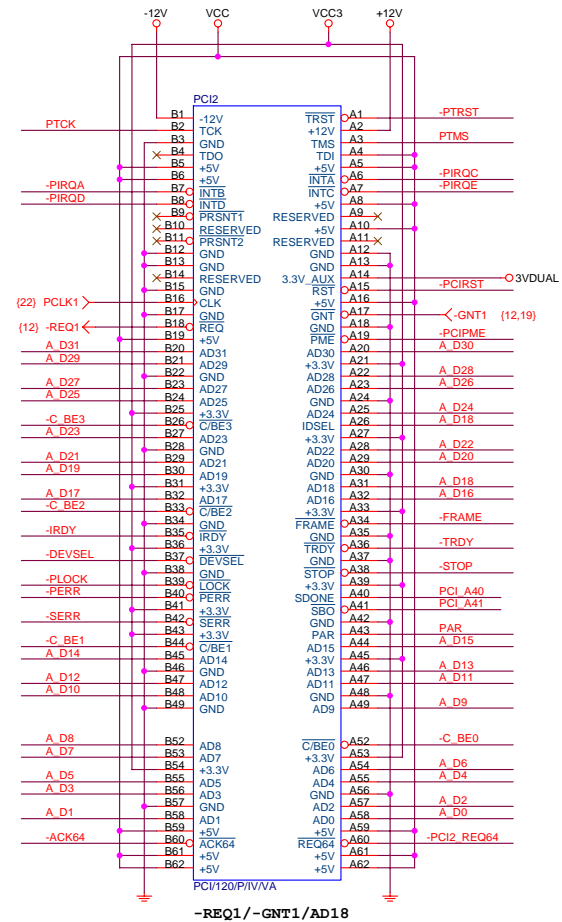
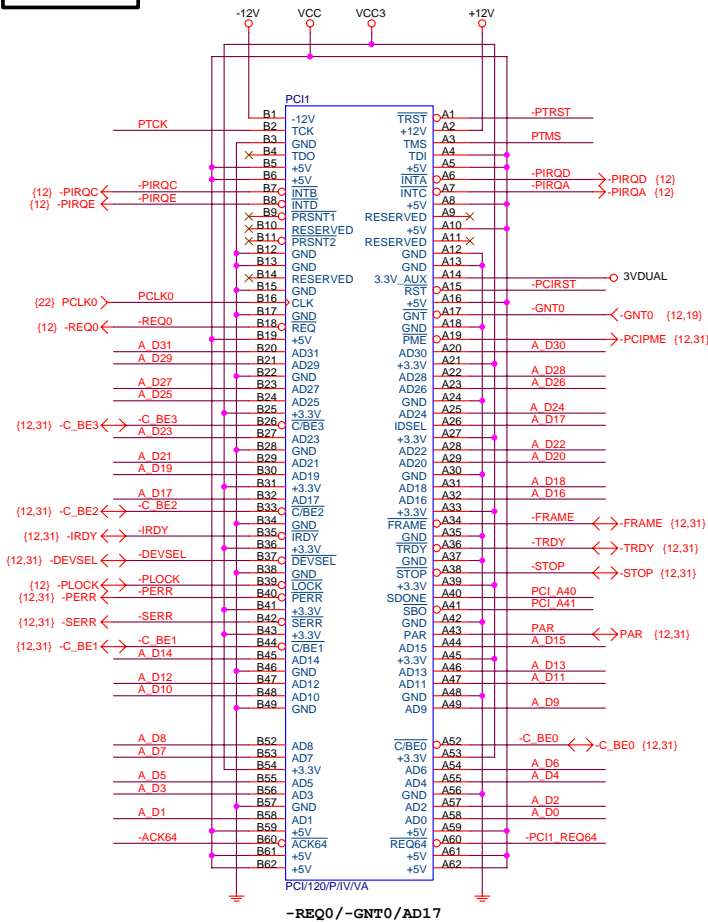


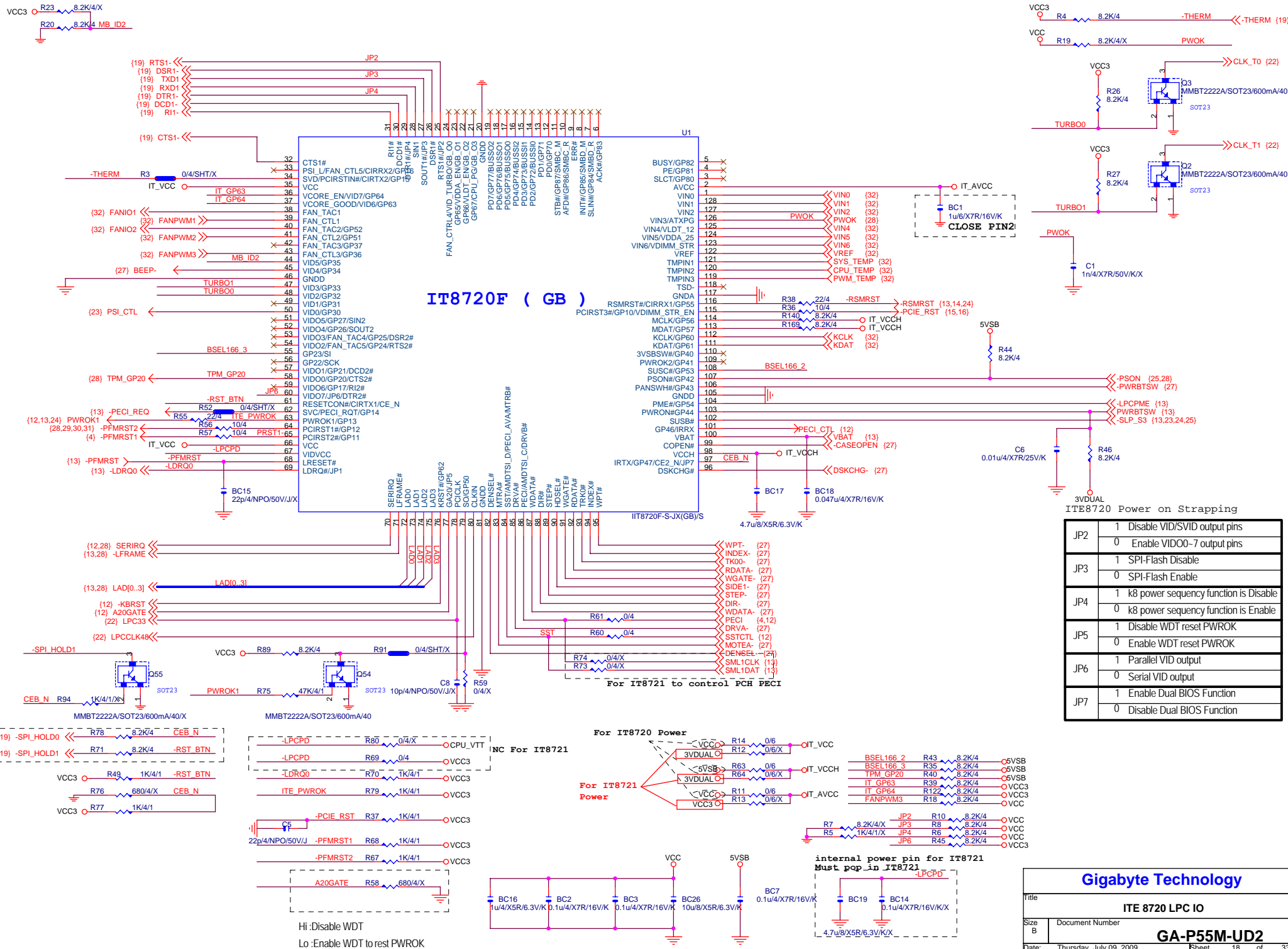




Gigabyte Technology			
Title			
PCI EXPRESS X 4 PORT			
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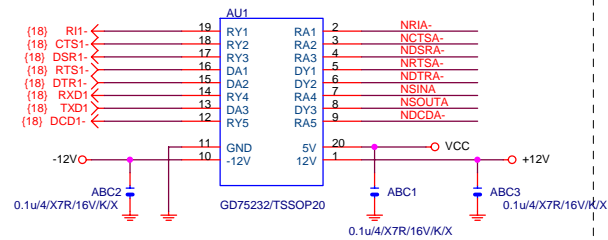
PCI1, 2 SLOT



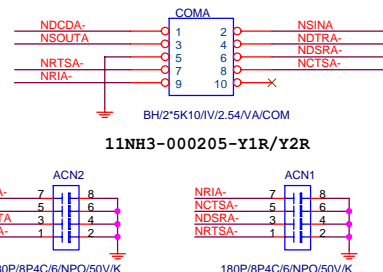
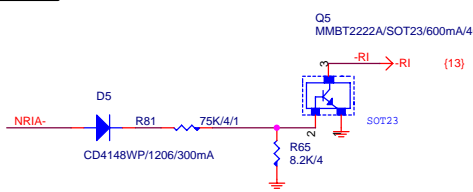


IT8720 Power on Strapping	
JP2	1 Disable VID/SVID output pins 0 Enable VID00-7 output pins
JP3	1 SPI-Flash Disable 0 SPI-Flash Enable
JP4	1 k8 power sequency function is Disable 0 k8 power sequency function is Enable
JP5	1 Disable WDT reset PWROK 0 Enable WDT reset PWROK
JP6	1 Parallel VID output 0 Serial VID output
JP7	1 Enable Dual BIOS Function 0 Disable Dual BIOS Function

COMA



COM RI



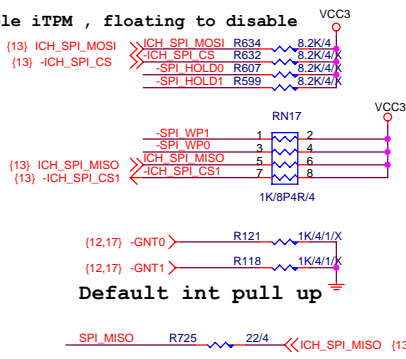
r1.0 DG;0.7 CRB

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating
0 means PD 1K

IC8SO-SOCKET need to check which is right

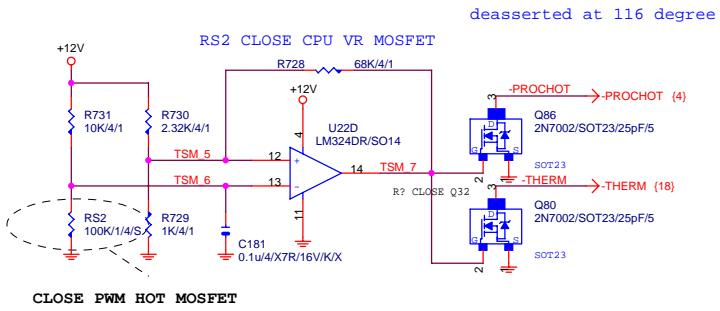
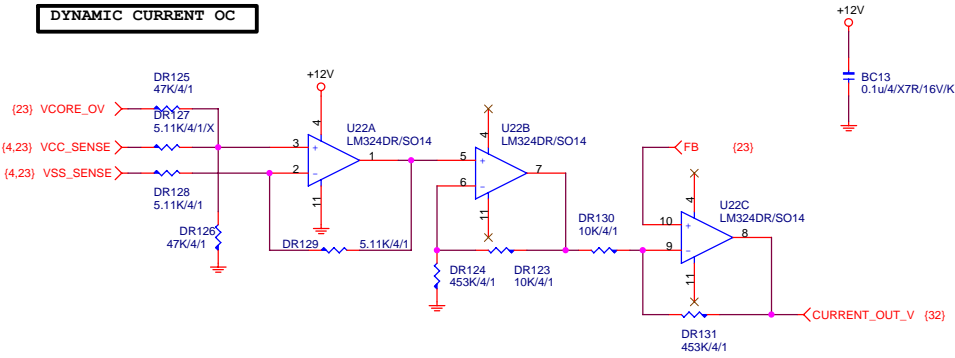
MOSI pull up to enable iTPM , floating to disable



Default int pull up

-PROHOT

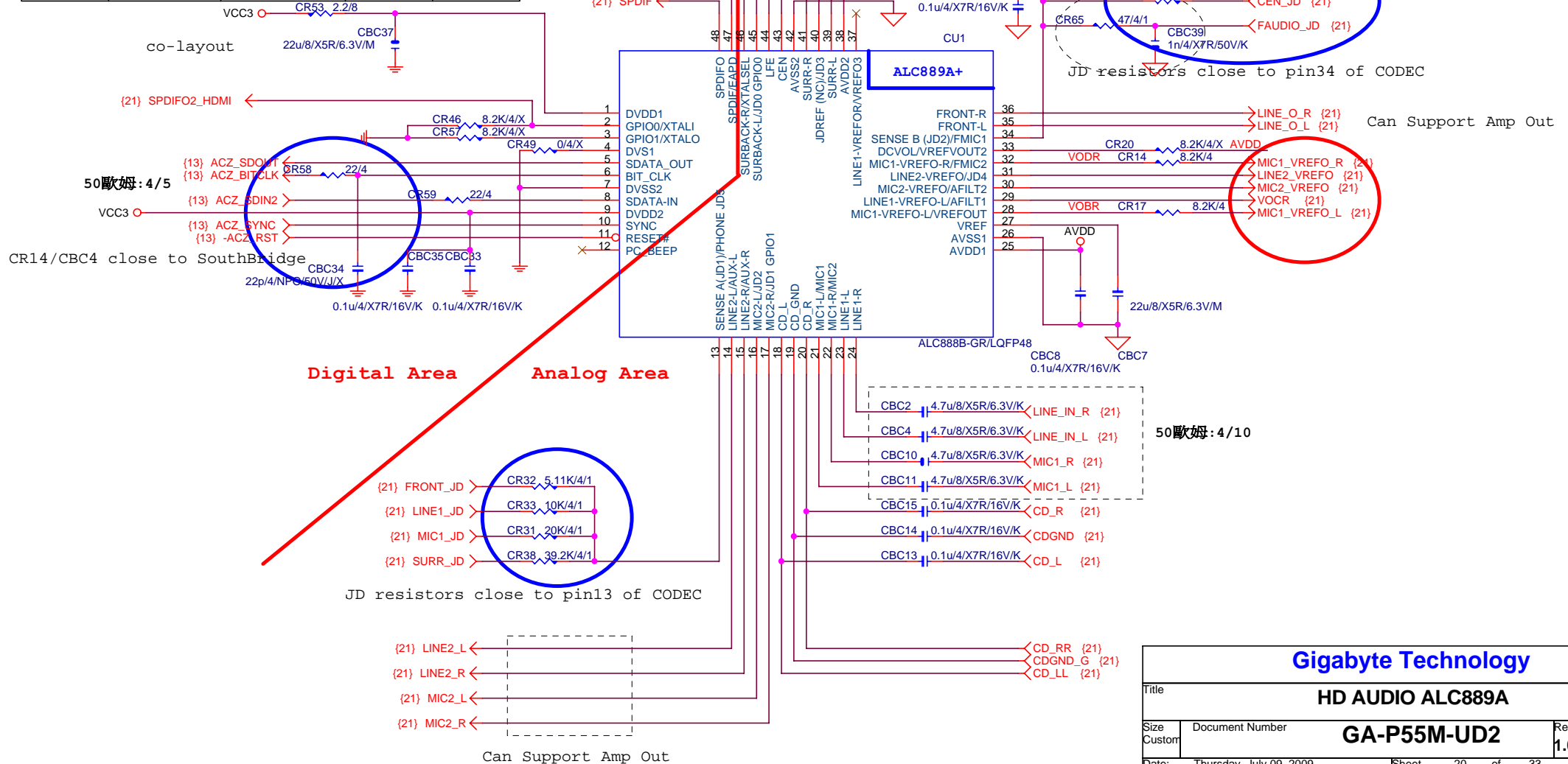
DYNAMIC CURRENT OC



AZALIA CODEC

ALC889A+/ALC889A/ALC888Vx Colay

	ALC888-VA ALC888-VC2	ALC888B	ALC889A	ALC889A+
CR49	O	X	O	O
CR46	X	X	O	X
CR57	X	X	O	X
CR47	X	X	X	O
CR48	O	O	O	X
CR26	20K/1%	20K/0.1%	20K/1%	20K/1%

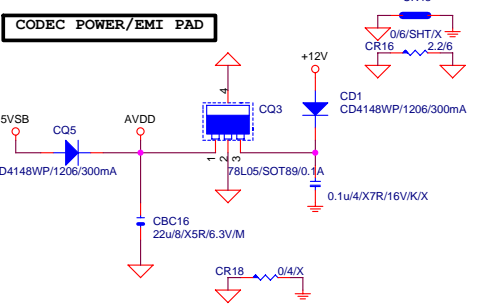


Gigabyte Technology

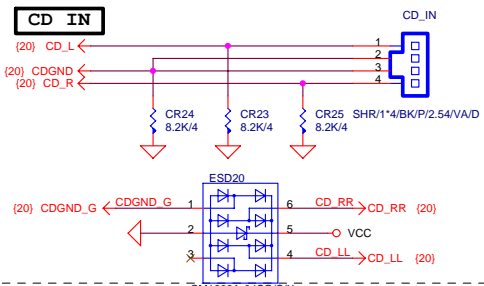
HD AUDIO ALC889A

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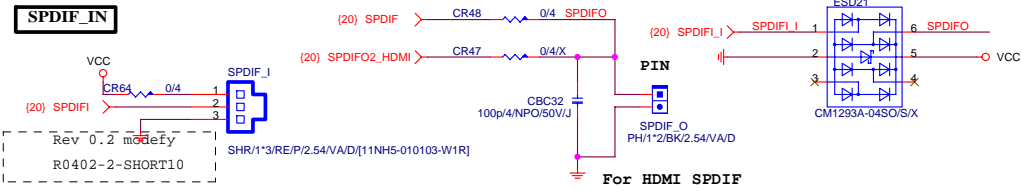
CODEC POWER/EMI PAD



CD IN

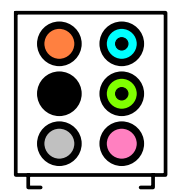


SPDIF_IN

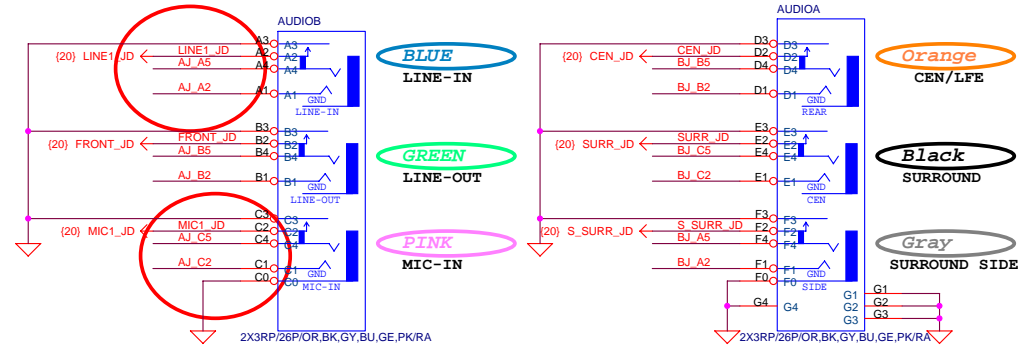


AZALIA JACK

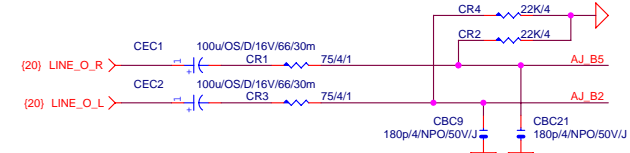
BTX AZALIA CONNECTOR



11NR6-403007-21R

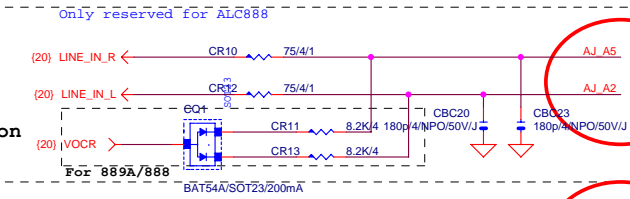


LINE-OUT

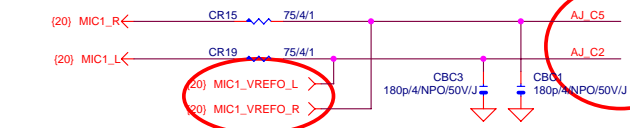


LINE-IN

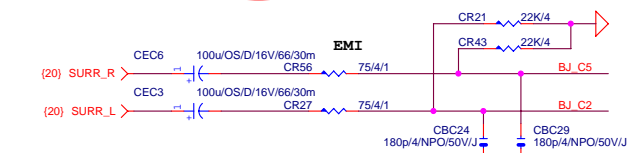
Verify MIC function in LINE-in



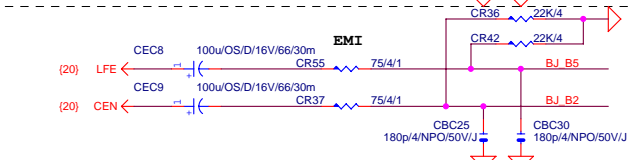
MIC-IN



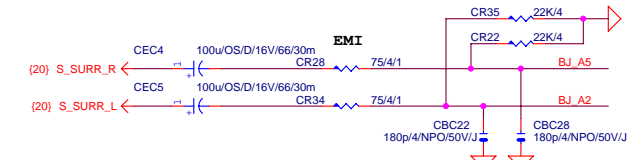
SURROUND



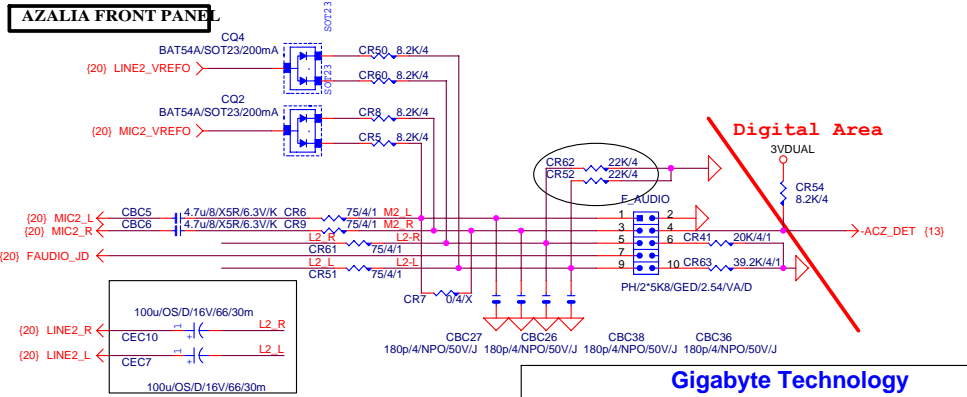
CEN/LFE



SURR BACK



AZALIA FRONT PANEL



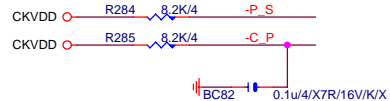
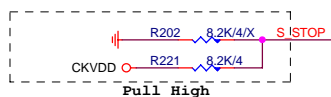
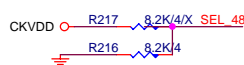
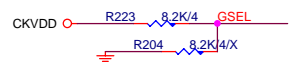
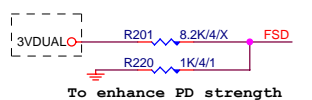
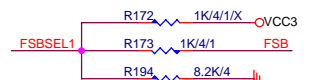
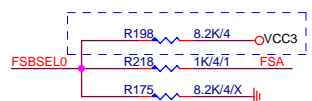
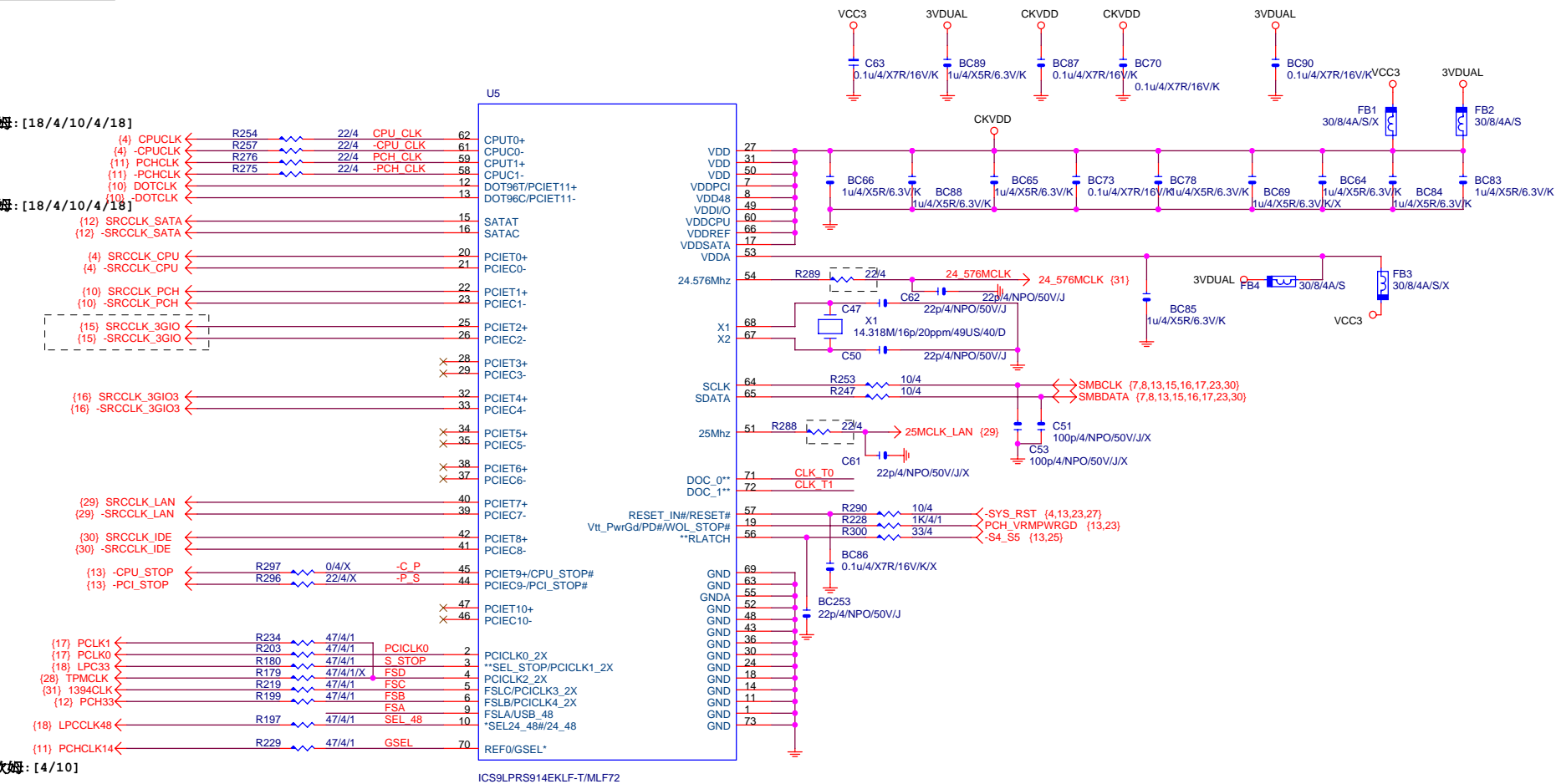
Gigabyte Technology

Title			AUDIO JACK
Document Number			GA-P55M-UD2
Size	Custom	Rev	1.0
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50歐姆: [18/4/10/4/18]

50歐姆: [4/10]

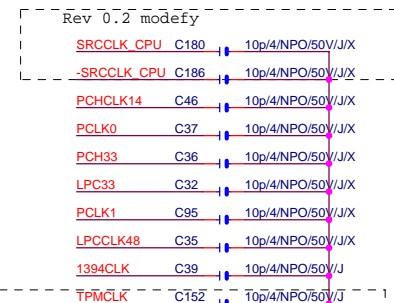
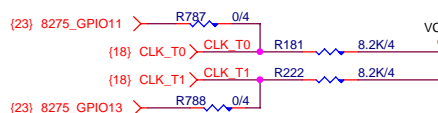


GSEL=1,96Mhz from 12/13
GSEL=0,100Mhz from 12/13

SEL_48=1, 24Mhz from pin10
SEL_48=0, 48Mhz from pin10

SEL_STOP: latched input to select pin functionality
1 = Selects pin 44/45 to be PCI_STOP#/CPU_STOP#
0 = Selects pin 44/45 to be PCIE outputs ;
3.3V PCICLK output

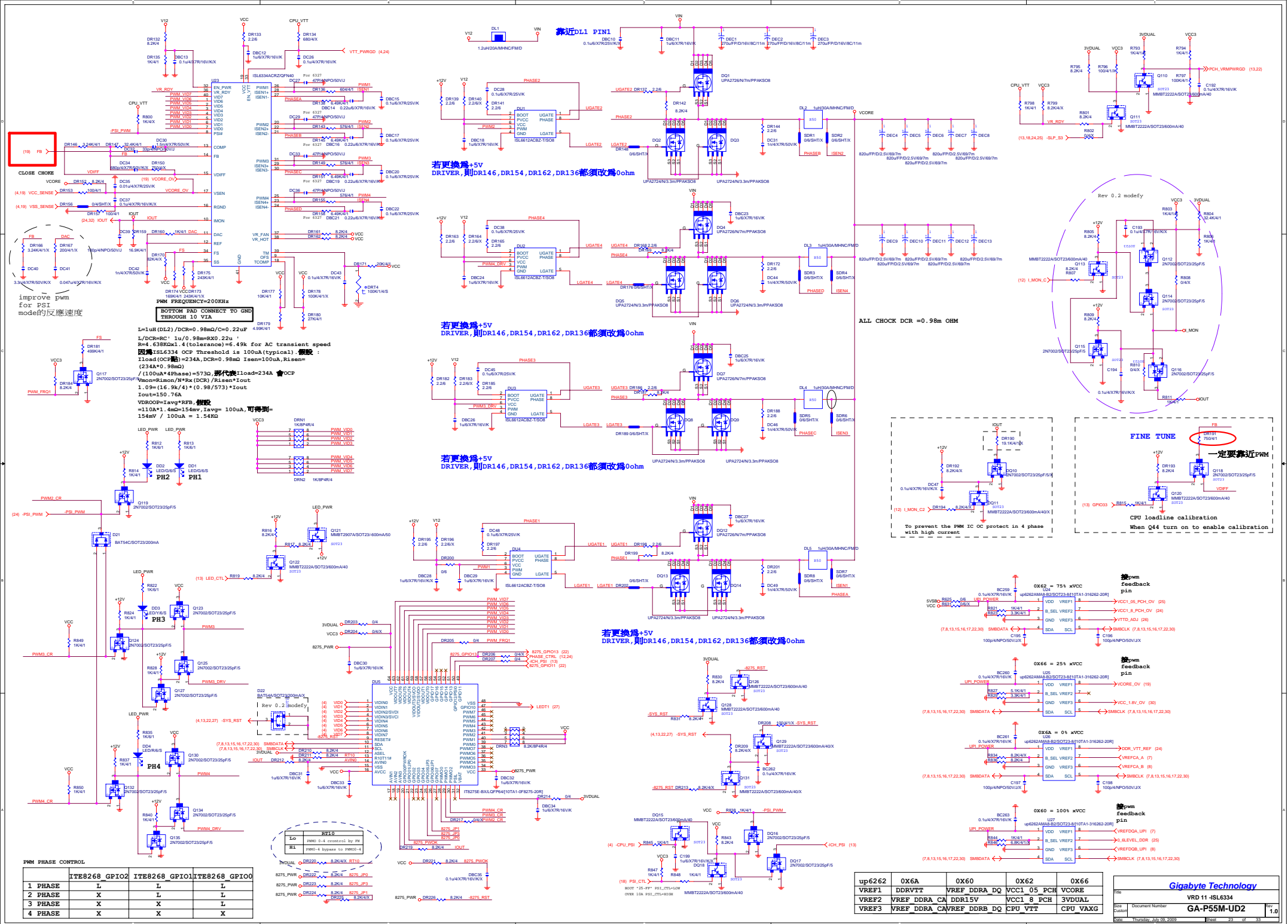
FSC	FSB	FSA	CPU
0	0	0	266MHz
0	0	1	133MHz
0	1	0	200MHz
0	1	1	166MHz
1	0	0	333MHz
1	1	0	400MHz



Rev 0.2 modify

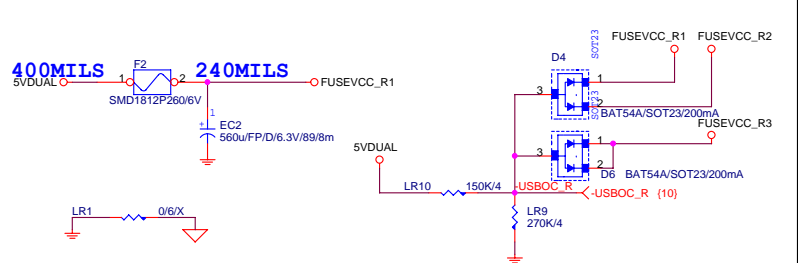
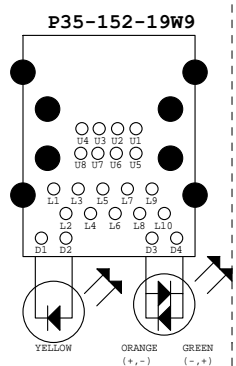
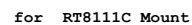
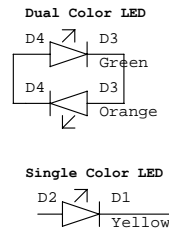
Gigabyte Technology

Title			CK505 CLK GEN
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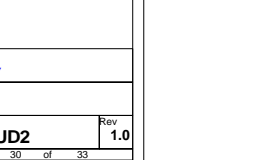
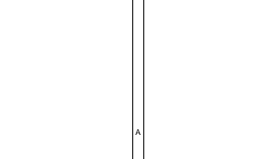
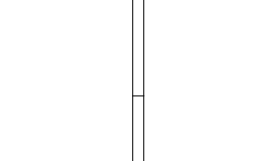
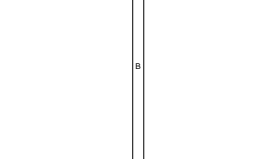
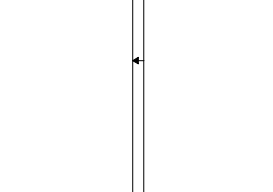
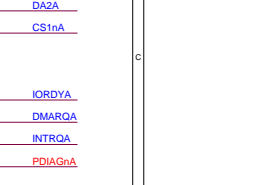
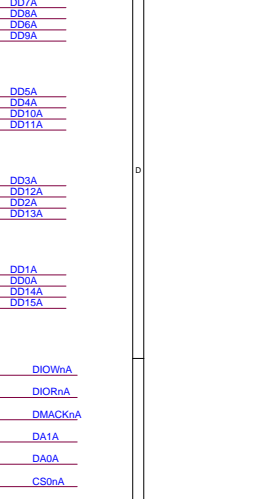
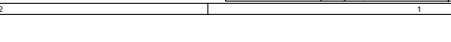
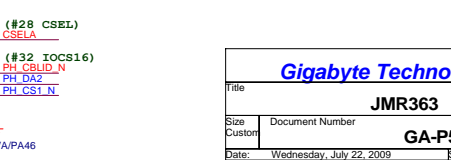
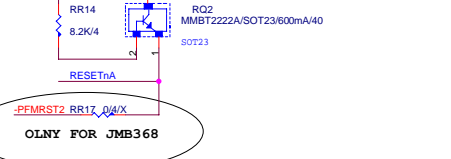
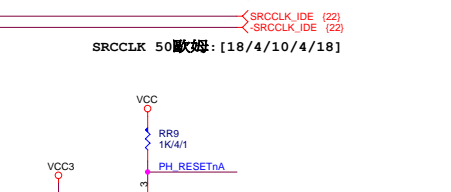
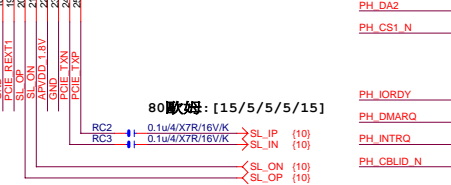
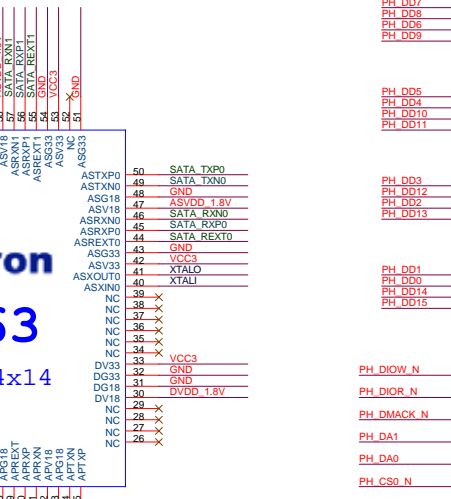
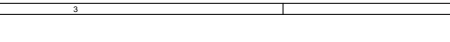
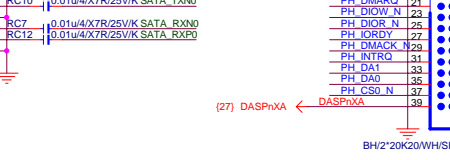
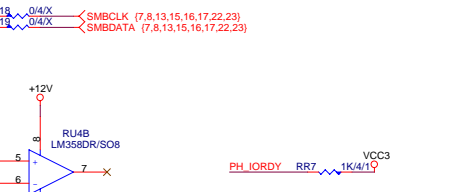
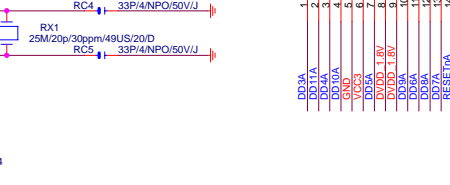
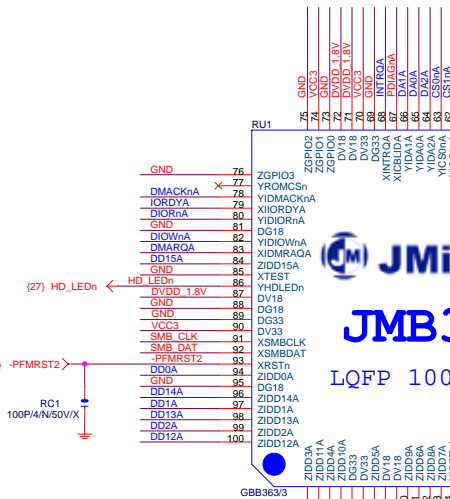
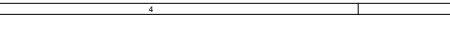
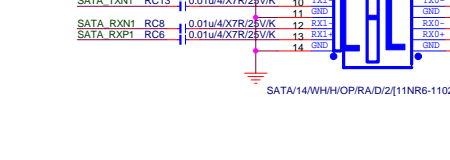
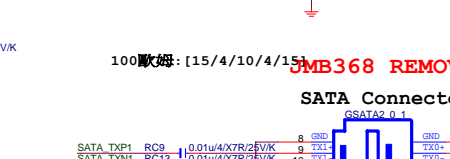
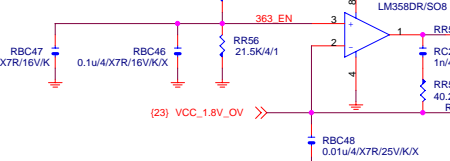
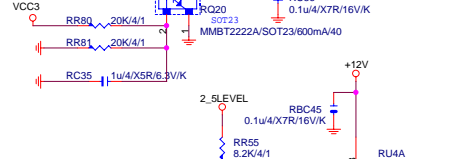
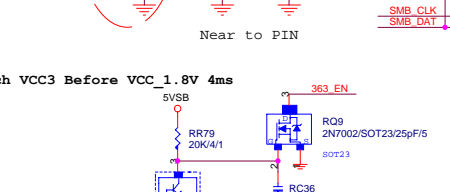
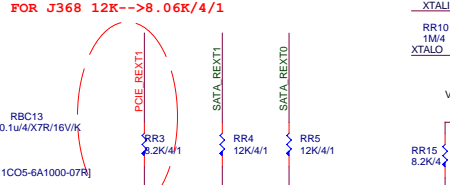
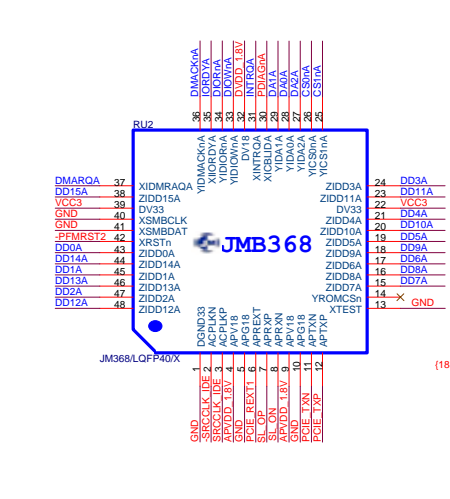
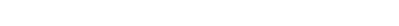
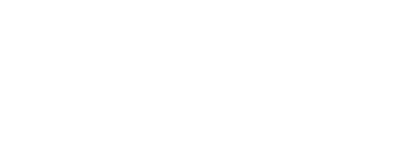
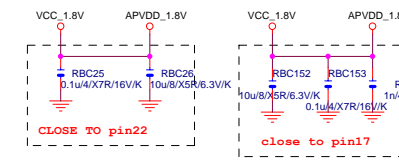
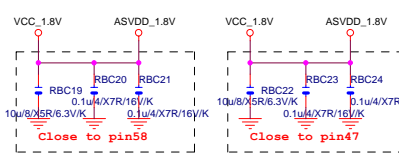
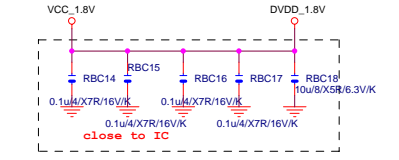
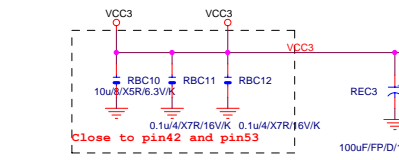
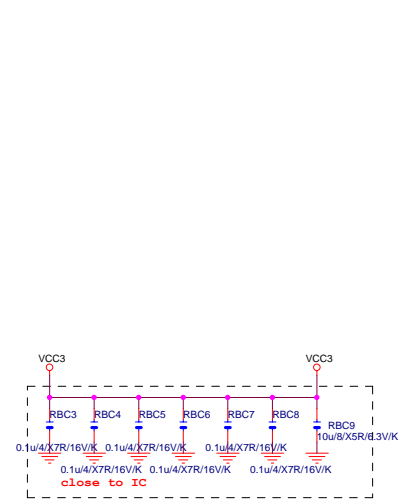
	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
VDD15	1.5V	1.2V

	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
VDD15	1.5V	1.2V

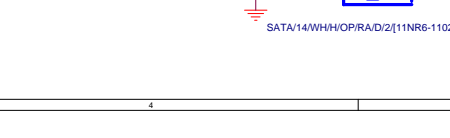
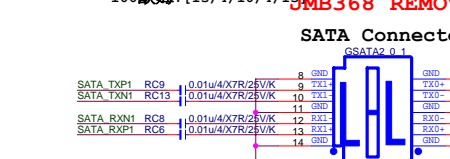
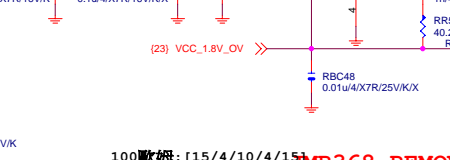
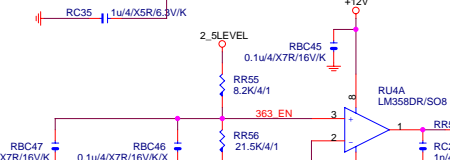
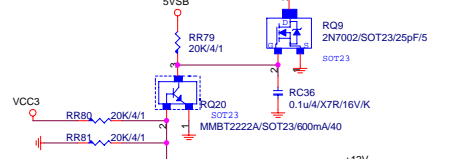


Title			
REALTEK RTL8111C			
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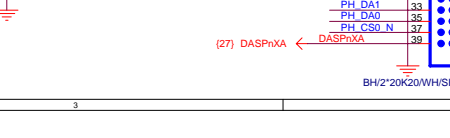
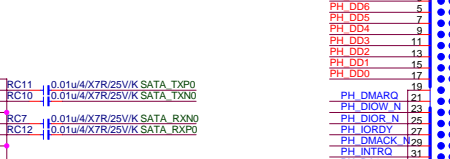
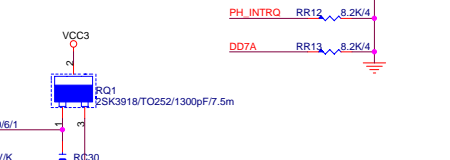
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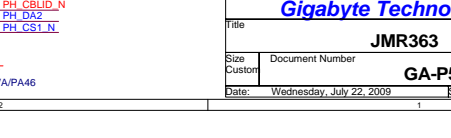
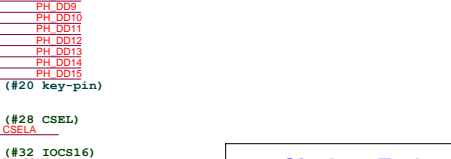
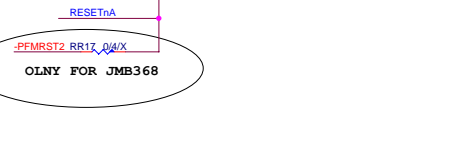
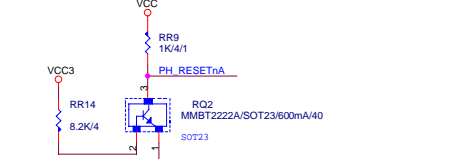
Patch VCC3 Before VCC_1.8V 4ms



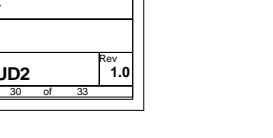
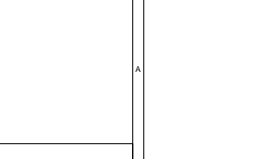
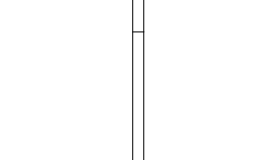
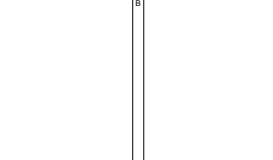
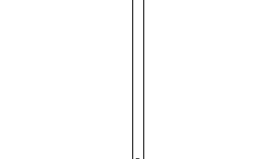
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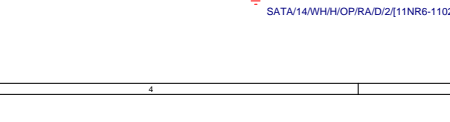
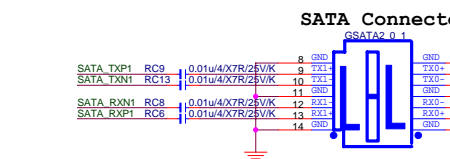
80歐姆: [15/5/5/5/15]



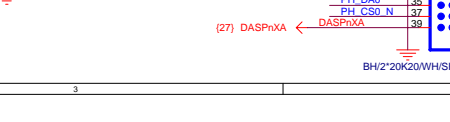
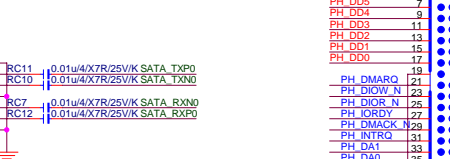
50歐姆: [18/4/10/4/18]



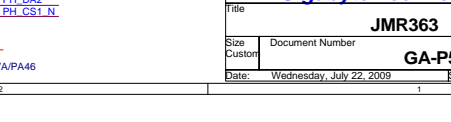
100歐姆: [15/4/10/4/15]



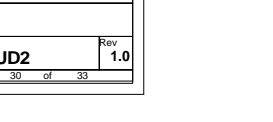
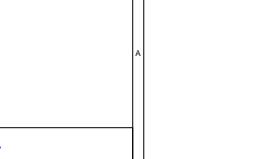
100歐姆: [15/4/10/4/15]



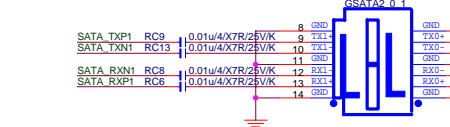
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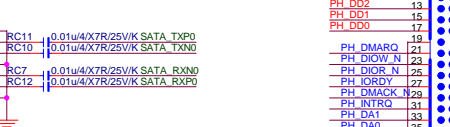
100歐姆: [15/4/10/4/15]



SATA Connector



IDE Connector



SATA14/WHH/OPRA/D12[11NR6-110214-51R]

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

SATA14/WHH/OPRA/D12[11NR6-110214-51R]

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

SATA14/WHH/OPRA/D12[11NR6-110214-51R]

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

(27) DASPhxA ← DASPhxA

SATA14/WHH/OPRA/D12[11NR6-110214-51R]

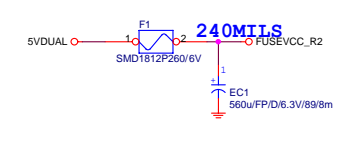
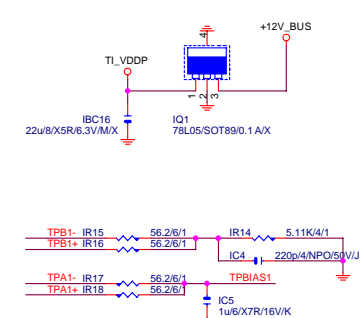
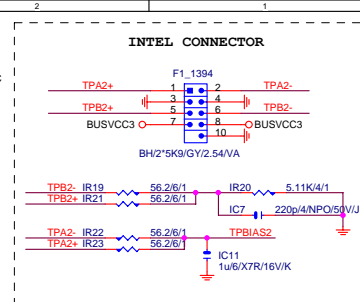
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(27) DASPhxA ← DASPhxA

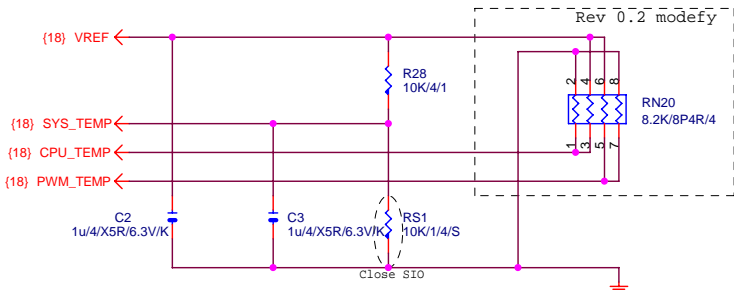
(27) DASPhxA ← DASPhxA

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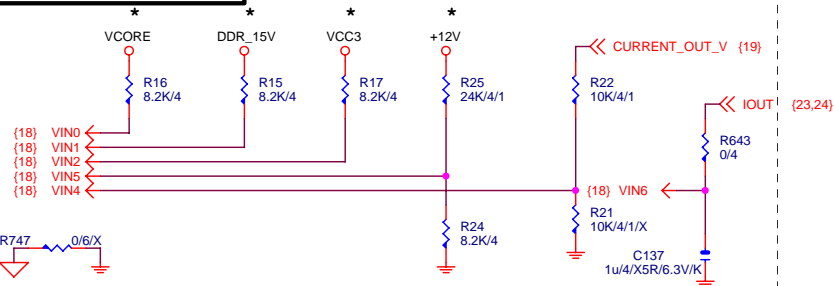
File	JMR363	Rev	1.0
Size	Document Number	GA-P55M-UD2	
Customer			
Date	Wednesday, July 22, 2009	Sheet	30 of 33



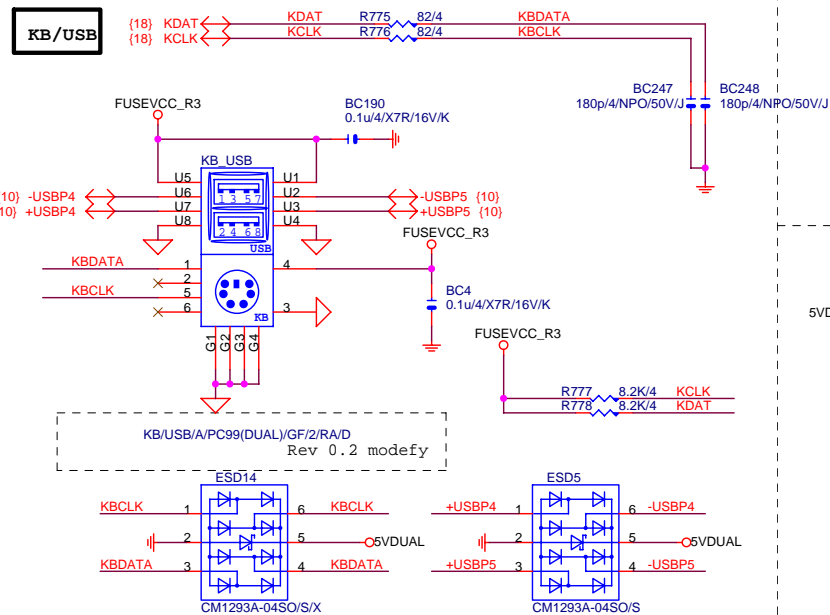
TEMP H/W MONITOR



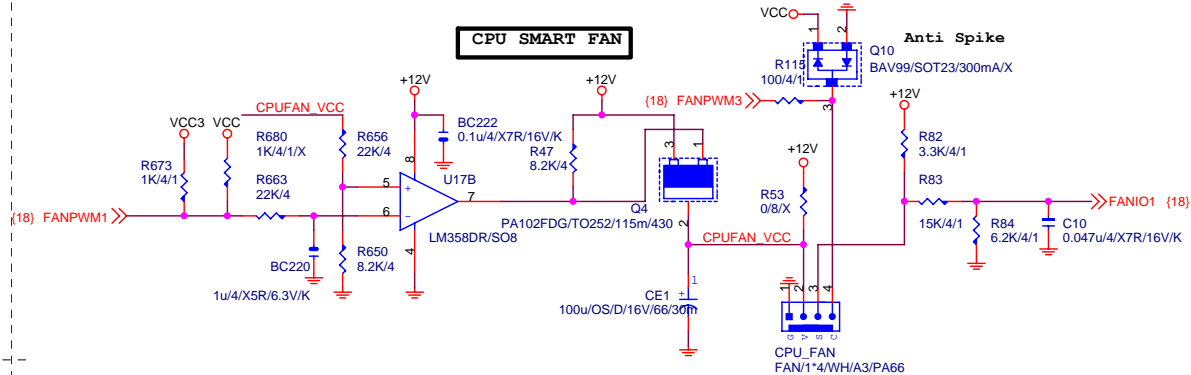
VOLTAGE-- H/W MONITOR



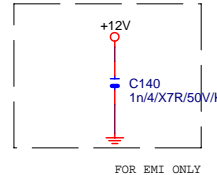
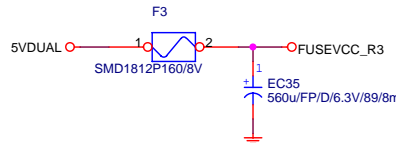
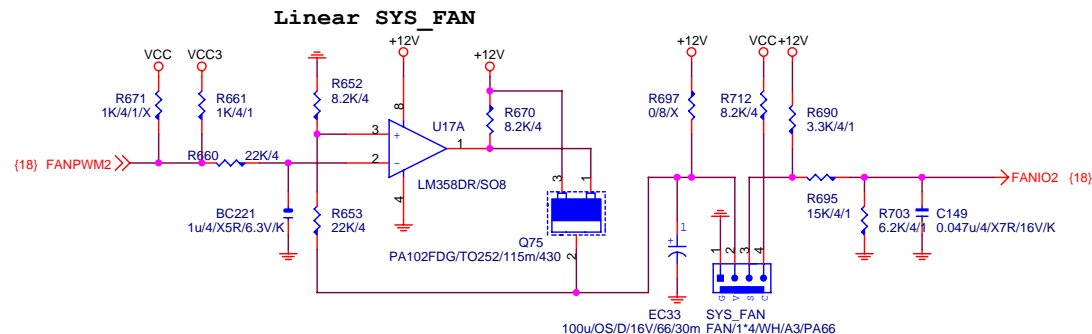
KB/USB



CPU SMART FAN



SYS FAN



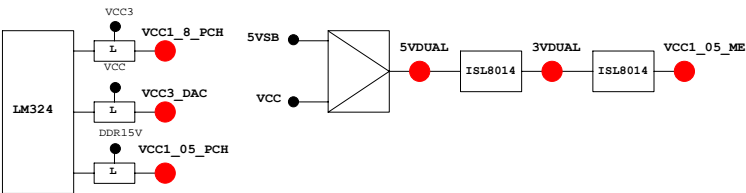
Gigabyte Technology

Title			HWM,KB/MS, FAN CTRL		
Size	Document Number		GA-P55M-UD2		Rev
Custom					1.0
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PIN NAME	PWR	AFTER POWER- DOWN	Default	USAGE	NOTE
GP0	MAIN	H-Z	GPI	-PECI_REQ	N/A
GP1/TACH1	MAIN		GPI	ICH_FAN_TACH1	N/A
GP2/PIRQE#	MAIN		GPI	-PIRQE	F/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI	-PIRQF	F/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI	-PIRQG	F/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI	-PIRQH	F/U 8.2K VCC3
GP6/TACH2	MAIN		GPI	ICH_FAN_TACH2	N/A
GP7/TACH3	MAIN		GPI	ICH_FAN_TACH3	N/A
GP8	STBY	H	GPO	GPI08	F/U 8.2K 3VDUAL
GP9/OC5#	STBY		NATIVE	OC5#	N/A
GP10/OC6#	STBY		NATIVE	OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE	-SMBALERT	F/U 8.2K 3VDUAL
GP12	STBY	L	GPI	LAN_PHY_PWR_CTRL	F/U 8.2K 3VDUAL
GP13	STBY	L	GPI	GPI013	F/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE	OC7#	N/A
GP15	STBY	L	GPO	GPI015	N/A
GP16	MAIN		GPI	-SKTOCC	F/U 8.2K VCC3
GP17/TACH0	MAIN		GPI	ICH_FAN_TACH0	N/A
GP18	MAIN		NATIVE	MB_ID0	F/D 8.2K GND
GP19	MAIN		GPI	-LAN1_ISO	F/U 8.2K VCC3
GP20	MAIN		NATIVE	LED_CTL	F/U 1K VCC3
GP21	MAIN		GPI	VCC18_PCH_OV2	F/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	VCORE_OV3	F/U 8.2K VCC3
GP23	MAIN		NATIVE	-LDRQ1	F/U 8.2K VCC3
GP24	STBY	L	GPO	TLS	F/U 8.2K 3VDUAL
GP25	STBY		NATIVE	-CPU_STOP	F/U 8.2K 3VDUAL
GP26	STBY		NATIVE	-ACZ_DET	F/U 8.2K 3VDUAL
GP27	STBY	H	GPO	GPI027	F/U 8.2K 3VDUAL
GP28	STBY	H	GPO	GPI028	F/U 8.2K 3VDUAL
GP29	STBY	L	GPI	GPI029	N/A
GP30	STBY	H-Z	GPI	S_PWR_ACK	F/U 100K 3VDUAL
GP31	STBY	H-Z	GPI	N/A(Reverse)	F/U 8.2K VCC3
GP32	MAIN	H	GPO	MB_ID1	F/D 8.2K GND
GP33	MAIN	H	GPO	LOAD-LINE	F/U 1K VCC3
GP34	MAIN	H-Z	GPI	-PCI_STOP	F/U 8.2K VCC3
GP35	MAIN	L	GPO	GPI035	F/U 8.2K VCC3
GP36	MAIN		GPI	-LANI_DSM	F/U 8.2K VCC3
GP37	MAIN		GPI	N/A	F/U 8.2K VCC3
GP38	MAIN	H-Z	GPI	VCORE_OV2	F/U 8.2K VCC3
GP39	MAIN	H-Z	GPI	-LAN_DSM	F/U 8.2K VCC3
GP40	STBY		NATIVE	OC1#	N/A
GP41	STBY		NATIVE	OC2#	N/A
GP42	STBY		NATIVE	OC3#	N/A
GP43	STBY		NATIVE	OC4#	N/A
GP44	STBY	L	NATIVE	N/A	F/U 8.2K 3VDUAL
GP45	STBY		NATIVE	-LPCPME	F/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	FWR_LED	F/U 8.2K 3VDUAL
GP47	STBY		NATIVE	PSI_LED	F/U 8.2K 3VDUAL
GP48	MAIN	H-Z	IN	EN_PWM	F/U 8.2K VCC3
GP49	MAIN	H-Z	IN	VCC18_OV1	F/U 8.2K VCC3
GP50	MAIN		NATIVE	-REQ1	F/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1	N/A
GP52	MAIN		NATIVE	-REQ2	F/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2	N/A
GP54	MAIN		NATIVE	-REQ3	F/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3	N/A
GP56	STBY		NATIVE	N/A(Reverse)	F/U 8.2K 3VDUAL
GP57	STBY	H-Z	IN	VCORE_OV1	F/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE	F_USB_OC	F/U 8.2K 3VDUAL
GP59	STBY		NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)	F/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE	-SUSTAT	N/A
GP62	STBY	L	NATIVE	SUSCLK	N/A
GP63	STBY	L	NATIVE	GPI063	N/A
GP64	MAIN	L	NATIVE	CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE	CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE	CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE	CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE	VCORE_OV4	F/U 8.2K 3VDUAL
GP73	STBY		NATIVE	1_05V_OV1	F/U 8.2K 3VDUAL
GP74	STBY	H-Z	NATIVE	1_05V_OV2	F/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE	N/A(Reverse)	F/U 8.2K 3VDUAL

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
FME#/GP54	-LPCPME	
PD5/GP75/BUSS00	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	~LAN1_DSM	NBT_LED1_C
S1CT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSX/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSI0	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VBSBW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSSO1	MB_ID3	
PD7/GP77/BUSSO2	MB_ID4	
AFD#/GP86/SMBC_R	SEC_FPIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GT1REF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSW#H#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTXX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	i_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSSO0	SB_LED3_C	



散熱模組料號:

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Termination
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH